

FACULTY OF INFORMATICS

B.E. 2/4 (IT) II Semester (Main) Examination, May/June 2011

COMPUTER ORGANIZATION AND MICROPROCESSOR

Time : 3 Hours]

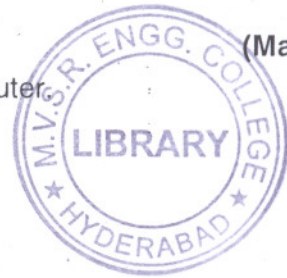
[Max. Marks : 75

Note : Answer **all** questions from Part A. Answer any **five** questions from Part B.

PART – A

(Marks : 25)

- | | | |
|-----|---|---|
| 1. | Give the basic operational concepts of a computer. | 2 |
| 2. | Write the advantages of CISC. | 2 |
| 3. | Write about multiprocessors. | 3 |
| 4. | Explain USB. | 3 |
| 5. | Mention some of memory management requirements. | 3 |
| 6. | Define cache memory. | 2 |
| 7. | Explain generate and propagate functions in carry and look ahead adder. | 3 |
| 8. | Explain shift and rotate instructions. | 3 |
| 9. | Write the functions of Interrupt routines. | 2 |
| 10. | Write features of module programming. | 2 |



PART – B

(Marks : 50)

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|-----|--|-------|
| 11. | (a) Write short notes on Interface circuits with example. | 5 |
| | (b) Explain about Interrupts. | 5 |
| 12. | (a) Explain about virtual memories. | 5 |
| | (b) Write short note on secondary storage. | 5 |
| 13. | Explain fast multiplication with example. | 10 |
| 14. | Explain addressing modes of 8086 with examples. | 10 |
| 15. | Explain Hardwired control and microprogrammed control units. | 10 |
| 16. | Explain different data transfer instructions with examples. | 10 |
| 17. | Write short notes on : | 5 + 5 |
| | (a) I/O design with example | |
| | (b) DMA | |