

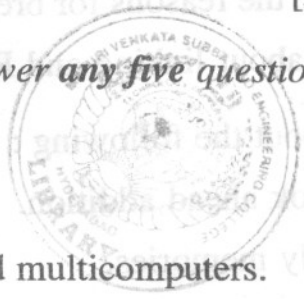


FACULTY OF INFORMATICS
B.E. 2/4 (IT) II Semester (Main) Examination, June 2010
COMPUTER ORGANIZATION AND MICROPROCESSORS

Time : 3 Hours]

[Max. Marks: 75

Note : Answer all questions from Part A. Answer any five questions from Part B.



PART – A

(Marks 25)

1. State the difference between multiprocessors and multicomputers. 2
2. Difference between subroutine and Interrupt service routine. 2
3. Write about memory hierarchy. 3
4. How to improve performance by INTER LEAVING of memory ? 3
5. List out the rule to perform addition/substraction of floating point numbers. 2
6. Write the control sequence for fetching a word from memory. 3
7. Difference between 8086 and 8088 micro processors. 2
8. What is the role of DAA instruction and describe with example. 3
9. What is a macro. 2
10. What are the different principal types of i/o transfers. 3

PART – B

(50 Marks)

11. Explain in detail about :
 - a) Enabling and Disabling interrupts. 5
 - b) Handling multiple devices with interrupts in a system. 5
12. What is the role of cache memory, what are the different mapping functions to be used in cache memory ? 10



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- 13. Discuss the process involved in executing an instruction by a processor. 10
- 14. a) Explain Assembly process in 8086. 5
b) Explain about addressing modes of 8086. 5
- 15. a) What are the reasons for breaking a program into small parts ? 2
b) Explain about linking and Relocation process in 8086. 8
- 16. Describe about the following : 10
 - a) Carry look ahead addition.
 - b) Read only memories.
- 17. Write a short note on the following : 10
 - a) Unconditional Branch instructions in 8086.
 - b) Historical perspective of a computer.