Code No. : 5440/N

FACULTY OF ENGINEERING
B.E. 2/4 (CSE) II Semester (New) (Main) Examination, May/June 2012
MICROPROCESSORS AND INTERFACING

Time: 3 Hours] [Max. Marks : 75

Note : *Answer **all** questions from Part **A**, Answer **any five** questions from Part **B**.*

PART – A **(25 Marks)**

1. Define Microprocessor and Microcomputer. **3**
2. List the 8085 hardware interrupts. **2**
3. Write a code to display digit 4 at Port 01 H. **3**
4. Draw the format of the mode set register of 8257. **3**
5. Write a short note on RS 232C. **2**
6. Define BSR mode in 8255. **2**
7. List the subroutine instructions of 8085. **2**
8. Compare memory mapped I/O and Peripheral mapped I/O. **3**
9. What are the various addressing modes in 8085 ? **2**
10. Register B has 93 H and the accumulater holds 15H. Illustrate the following : **3**
    i) ORA B
    ii) CMA.

PART – B **(50 Marks)**

11. Explain the 8085 microprocessor architecture with a functional block diagram. **10**
12. Draw the explain the timing diagram for OUT instruction. **10**
13. Discuss the function of 8279 with a neat diagram. **10**

14. Explain about DAC interfacing with 8051. 10

15. a) Describe the addressing modes in 8051 with examples. 5

    b) Write an assembly language program to arrange the 'n' numbers in ascending order. 5

16. What are the various instructions in 8086 ? Explain with examples. 10

17. Write a short note on the following :

    a) 8085 Rotate Instructions. 4

    b) DMA. 3

    c) 8086 Flag Register. 3