

MVSR ENGINEERING COLLEGE – NADERGUL
Department of Electrical & Electronics Engineering
Time Table for B.E. CBCS IV Semester EEE (SEC-A) 2018-2019

Room No: M-105

w.e.f. 24/12/2018

DAY	9.30 – 10.30	10.30 – 11.30	11.30 – 12.30	12.30 – 1.15	1.15 – 2.15	2.15 – 3.15	3.15 – 4.15
MON	PS-I	EM –I	PE	LUNCH (12.30-1.15)	LIC	DEIC Lab (A) (YLN) CAED Lab (B) (DHK/BRK)	
TUE	EM-IV	MEA	EM –I		EC-II	DEIC Lab (B) (YLN/VRK) CAED Lab (C) (DHK)	
WED	EM-IV	EC-II	PE		PS-I	LIC	MEA
THU	← CRT →		DEIC Lab (C) (YLN/VRK) (11.40-1.40) CAED Lab (A) (DHK/BRK)		LUNCH (1.40-2.15)	MEA	PS-I
FRI	PE	EM –I	EC-II	LUNCH (12.30-1.15)	LIC	EM-IV	---
SAT	EC-II	EM-IV	EM -I		PE	---	---

A, B, C in brackets indicate batches

Legend:

S. No.	Subject Code		Subject	Staff Name
1	BS401MT	EM-IV	Engineering Mathematics-IV	Mr. U. Shanmukha Rao
2	PC401EE	EC-II	Electrical Circuits – II	Mr. C.V.G.K. Rao
3	PC402EE	EM –I	Electrical Machines-I	Dr. D.V.M. Chary
4	PC403EE	PS-I	Power Systems-I	Mr. I. Narasimha Swamy
5	PC404EE	PE	Power Electronics	Mr. V.V. Satyanarayana A
6	PC405EE	LIC	Linear Integrated Circuits	Mr. Y.L.N. Rao
7	HS401BM	MEA	Managerial Economics & Accountancy	Mr. Ch. Venkat Rao
8	PC451EE	DEIC Lab	Digital Electronics and Integrated Circuits Lab	Mr. Y.L.N. Rao / Mr. V. Raghu Krishna
9	PC452EE	CAED Lab	Computer Aided Electrical Drawing Lab.	Dr. D. Hari Krishna / Mr. B. Rama Krishna

Timetable Coordinator

Chief Timetable Coordinator

HOD (EEE)

Principal

MVSR ENGINEERING COLLEGE – NADERGUL
Department of Electrical & Electronics Engineering
Time Table for B.E. CBCS IV Semester EEE (SEC-B) 2018-2019

Room No: M-106

w.e.f. 24/12/2018

DAY	9.30 – 10.30	10.30 – 11.30	11.30 – 12.30	12.30 – 1.15	1.15 – 2.15	2.15 – 3.15	3.15 – 4.15
MON	DEIC Lab (A) (VRK/YLN) CAED Lab (B) (GS)		EM –I	LUNCH (12.30-1.15)	EC-II	PS-I	PE
TUE	EM –I	EC-II	EM-IV		LIC	PE	MEA
WED	LIC	EM-IV	PS-I		MEA	DEIC Lab (B) (VRK) CAED Lab (C) (GS/DHK)	
THU	LIC	EC-II	← CRT → (11.40-1.40)		LUNCH (1.40-2.15)	DEIC Lab (C) (VRK/YLN) CAED Lab (A) (GS/DHK)	
FRI	PS-I	EM –I	PE	LUNCH (12.30-1.15)	EM-IV	MEA	---
SAT	EM-IV	EM –I	EC-II		PE	---	---

A, B, C in parentheses indicate batch

Legend:

S. No.	Subject Code		Subject	Staff Name
1	BS401MT	EM-IV	Engineering Mathematics-IV	Mrs. D. Bhagya
2	PC401EE	EC-II	Electrical Circuits - II	Mr. C.V.G.K. Rao
3	PC402EE	EM -I	Electrical Machines-I	Mr. G. Satyanarayana
4	PC403EE	PS-I	Power Systems-I	Mrs. G. Sirisha
5	PC404EE	PE	Power Electronics	Dr. T. Chandra Shekar
6	PC405EE	LIC	Linear Integrated Circuits	Mr. Y.L.N. Rao
7	HS401BM	MEA	Managerial Economics & Accountancy	Mr. Ch. Venkat Rao
8	PC451EE	DEIC Lab	Digital Electronics and Integrated Circuits Lab	Mr. V. Raghu Krishna / Mr. Y.L.N. Rao
9	PC452EE	CAED Lab	Computer Aided Electrical Drawing Lab.	Mrs. G. Sirisha / Dr. D. Hari Krishna

Timetable Coordinator

Chief Timetable Coordinator

HOD (EEE)

Principal

MVSR ENGINEERING COLLEGE – NADERGUL
Department of Electrical & Electronics Engineering
Time Table for B.E. CBCS VI Semester EEE (SEC-A) 2018-2019

Room No: M-205

w.e.f. 24/12/2018

DAY	9.30 – 10.30	10.30 – 11.30	11.30 – 12.30	12.30 – 1.15	1.15 – 2.15	2.15 – 3.15	3.15 – 4.15
MON	RET	EM-III	MPMC	LUNCH	EDS	SGP	---
TUE	← CRT →		EM Lab-II (A) (NSRK/PBGP) DSP Lab (B) (TCS/LHV) CS Lab (C) (NR/GS)		LUNCH 1.40-2.15	RET	---
WED	EM Lab-II (B) (NSRK/PBGP) DSP Lab (C) (TCS/LHV) CS Lab (A) (NR/MRR)		MPMC	LUNCH	EM-III	EM-III	---
THU	SGP	EDS			EM-III	RET	DMM / OOP
FRI	EM Lab-II (C) (NSRK/PBGP) DSP Lab (A) (TCS/LHV) CS Lab (B) (NR/MRR)				EDS	SGP	MPMC
SAT	DMM / OOP	DMM / OOP	MC	MC	LUNCH 1.40-2.15	MC	MC

A, B, C in brackets indicate batches

Legend:

S. No.	Subject Code	Subject		Staff Name
1	PC601EE	EM-III	Electrical Machines-III	Mr. N. Siva Rama Krishna
2	PC602EE	MPMC	Microprocessors and Microcontrollers	Mrs. D. Sai Prasanna
3	PC603EE	SGP	Switchgear and Protection	Mr. V. Raghu Krishna
4	PC604EE	RET	Renewable Energy Technologies	Mr. P.B. Guru Prasanna
5	PE-II	EDS	Electrical Distribution Systems	Dr. D. Hari Krishna
6	OE602CS OE601CE	OOP /DMM	OOPs JAVA / Disaster Management & Mitigation	Mr. Mr. G. Srisailam /Ms. Shwetha Koushik
7	PC651EE	EM Lab-II	Electrical Machines lab-II	Mr. N. Siva Rama Krishna/ Mr. P.B. Guru Prasanna
8	PC652EE	DSP Lab	Digital signal Processing Lab	Dr. T. Chandra Shekar / Mr. L. Harsha Vardhan
9	PC653EE	CS Lab	Control systems Lab	Mr. N. Ravi / Mr. M. Raveendra Reddy/ Mrs. G. Sirisha

Timetable Coordinator

Chief Timetable Coordinator

HOD (EEE)

Principal

MVSR ENGINEERING COLLEGE – NADERGUL
Department of Electrical & Electronics Engineering
Time Table for B.E. CBCS VI Semester EEE (SEC-B) 2018-2019

Room No: M-210

w.e.f. 24/12/2018

DAY	9.30 – 10.30	10.30 – 11.30	11.30 – 12.30	12.30 – 1.15	1.15 – 2.15	2.15 – 3.15	3.15 – 4.15
MON	EM-III	EDS	RET	LUNCH	SGP	MPMC	---
TUE	EM Lab-II (A) (INS/BRK) DSP Lab (B) (PVVR/TCS) CS Lab (C) (MRR/GS)		← CRT → 11.40-1.40			---	---
WED	SGP	EDS	EM-III	LUNCH	MPMC	RET	---
THU	EM Lab-II (B) (INS/BRK) DSP Lab (C) (PVVR/TCS) CS Lab (A) (MRR/GS)		MPMC		EM-III	RET	DMM / OOP
FRI	EDS	MPMC	SGP		EM-III	EM Lab-II (C) (INS/BRK) DSP Lab (B) (PVVR/GSN) CS Lab (A) (MRR/GS)	
SAT	DMM / OOP	DMM / OOP	MC	MC	LUNCH 1.40-2.15	MC	MC

A, B, C in brackets indicate batches

Legend:

S. No.	Subject Code		Subject	Staff Name
1	PC601EE	EM-III	Electrical Machines-III	Dr. E.V.C. Sekhara Rao
2	PC602EE	MPMC	Microprocessors and Microcontrollers	Mr. P.V.V. Raghava Sharma
3	PC603EE	SGP	Switchgear and Protection	Mr. S. Shyam Mohan
4	PC604EE	RET	Renewable Energy Technologies	Mr. M. Raveendra Reddy
5	PE-II	EDS	Electrical Distribution Systems	Dr. D. Hari Krishna
6	OE602CS OE601CE	OOP /DMM	OOPs JAVA / Disaster Management & Mitigation	Mr. G. Srisailam /Ms. Shwetha Koushik
7	PC651EE	EM Lab-II	Electrical Machines lab-II	Mr. I. Narasimha Swamy / Mr. B. Rama Krishna
8	PC652EE	DSP Lab	Digital signal Processing Lab	Mr. P.V.V. Raghava Sharma/ Dr. T. Chandra Shekar / Mr. G. Satya Naryanana
9	PC653EE	CS Lab	Control systems Lab	Mr. M. Raveendra Reddy/ Mrs. G. Sirisha

Timetable Coordinator

Chief Timetable Coordinator

HOD (EEE)

Principal

MVSR ENGINEERING COLLEGE – NADERGUL
Department of Electrical & Electronics Engineering
Time Table for IV/IV II Semester B.E. EEE (Sec-A) 2018-2019

Room No: M-102

w.e.f. 24/12/2018

DAY	9.45 – 10.35	10.35 – 11.25	11.25 – 12.15	12.15 – 1.00	1.00 – 1.45 LUNCH	1.45 – 2.35	2.35 – 3.25	3.25 – 4.15
MON	DSP Lab (A) (PVVR/CVGK)			EPDE		UT	RES	RES
TUE	RES	EPDE	EPDE	UT		UT	IAFM	IAFM
WED	DSP Lab (B) (PVVR)			RES		SEMINAR		
THU	EPDE	IAFM	IAFM	UT		DSP Lab (C) (PVVR/CVGK)		
FRI	PROJECT					PROJECT		

A, B, C in parentheses indicate batch

Legend:

S. No.	Subject Code	Subject		Staff Name
1	EE 451	UT	Utilization	Mr. M. Raveendra Reddy
2	EE 461	EPDE	Electrical Power Distribution Engineering	Mr. N. Bharat Mohan
3	EE 471	RES	Renewable Energy Sources	Mr. P.B. Guru Prasanna
4	ME 472	IAFM	Industrial Administration and Financial Management	Mr. P.V. Durga Prasad
5	EE 481	DSP LAB	Digital Signal Processing Lab	Mr. P.V.V. Raghava Sharma / Mr. C.V.G.K. Rao
6	EE 483	SEM	SEMINAR	Mr. C.V.G.K. Rao
7	EE 482	PROJECT	PROJECT	

Timetable Coordinator

Chief Timetable Coordinator

HOD (EEE)

Principal

MVSR ENGINEERING COLLEGE – NADERGUL
Department of Electrical & Electronics Engineering
Time Table for IV/IV II Semester B.E. EEE (Sec-B) 2018-2019

Room No: M-103

w.e.f. 24/12/2018

DAY	9.45 – 10.35	10.35 – 11.25	11.25 – 12.15	12.15 – 1.00	1.00 – 1.45 LUNCH	1.45 – 2.35	2.35 – 3.25	3.25 – 4.15
MON	EPDE	RES	IAFM	UT		DSP Lab (A) (GSN/VVS)		
TUE	UT	RES	EPDE	EPDE		SEMINAR		
WED	IAFM	IAFM	RES	UT		DSP Lab (B) (GSN/VVS)		
THU	DSP Lab (C) (GSN)			RES		UT	EPDE	IAFM
FRI	PROJECT					PROJECT		

A, B, C in parentheses indicate batch

Legend:

S. No.	Subject Code	Subject		Staff Name
1	EE 451	UT	Utilization	Dr. D.V.M. Chary
2	EE 461	EPDE	Electrical Power Distribution Engineering	Mr. G. Satyanarayana
3	EE 471	RES	Renewable Energy Sources	Mr. S. Shyam Mohan
4	ME 472	IAFM	Industrial Administration and Financial Management	Ms. N. Haritha
5	EE 481	DSP LAB	Digital Signal Processing Lab	Mr. G. Satyanarayana / Mr. V.V. Satyanarayana A
6	EE 483	SEMINAR	SEMINAR	Dr. E.V.C. Sekhara Rao
7	EE 482	PROJECT	PROJECT	

Timetable Coordinator

Chief Timetable Coordinator

HOD (EEE)

Principal