

**MVSR ENGINEERING COLLEGE – NADERGUL**  
**Department of Electrical & Electronics Engineering**  
**Time Table for B.E. III Semester EEE (SEC-A) 2017-2018**

Room No: M-105

w.e.f. 16/08/2017

DAY	9.30 – 10.30	10.30 – 11.30	11.30 – 11.40	11.40 – 12.40	12.40 – 1.40	1.40 – 2.15	2.15 – 3.15	3.15 – 4.15
MON	DELD	EE-II		EMF	EC-I		EE LAB (A) (CLS / MD)	
TUE	EC-I	EMF		DELD	EM-III		EE LAB (B) (CLS / MD)	
WED	EM-III	EVS		PMP	EMF		EE LAB (C) (CLS / MD)	
THU	EE-II	EM-III		EVS	DELD		EC-I	EMF
FRI	EVS	EE-II		EC-I	EM-III		PMP	PMP
SAT	ME LAB (B) (PVDP/MRK)		<b>BREAK</b>	ME LAB (C) (PVDP/MRK)		<b>LUNCH</b>	ME LAB (A) (PVDP/MRK)	

A,B,C in brackets indicate batches

**Legend:**

S. No.	Subject		Staff Name
1	EM-III	Engineering Mathematics-III	Ms. P. Swapna
2	EE-II	Electronic Engineering-II	Mr. C. Laxmana Sai
3	PMP	Prime Movers & Pumps	Mr. G. Srinivas Sharma
4	EC-I	Electrical Circuits-I	Mr. C. V. G. K. Rao
5	EMF	Electromagnetic Fields	Mr. VV. Satyanarayana Akula
6	DELD	Digital Electronics & Logic Design	Ms. K. Navya Tejaswini
7	EVS	Environmental Sciences	Mr. A. Sanjay Kumar
8	EE LAB	Electronic Engineering Lab	Mr. C. Laxmana Sai / Ms. M. Deepthi
9	ME LAB	Mechanical Engineering Lab	Dr. P.V. Durga Prasad / Mr. M. RaviKumar

**Timetable coordinator**

**Chief Timetable Coordinator**

**HOD (EEE)**

**Principal**

**MVSR ENGINEERING COLLEGE – NADERGUL**  
**Department of Electrical & Electronics Engineering**  
**Time Table for B.E. III Semester EEE (SEC-B) 2017-2018**

Room No: M-106

w.e.f. 16/08/2017

DAY	9.30 – 10.30	10.30 – 11.30	BREAK 11.30 – 11.40	11.40 – 12.40	12.40 – 1.40	LUNCH 1.40 – 2.15	2.15 – 3.15	3.15 – 4.15
MON	EE LAB (A) (EVNL/MVK) / ME LAB (B) (GSS/VSR)			PMP (BKM)	EM-III		EE-II	EVS
TUE	EVS	EC-I		DELD	EMF		EE-II	EM-III
WED	EM-III	EMF		EE LAB (B) (EVNL/MVK) / ME LAB (C) (GSS/VSR)			DELD	PMP (SR)
THU	EE-II	EC-I		EC-I	EMF		EVS	EM-III
FRI	EE LAB (C) (EVNL/MVK) / ME LAB (A) (GSS/VSR)			PMP (SR)	EMF		---	---
SAT	EC-I	DELD						

A, B, C in parentheses indicate batch

**Legend:**

S. No.	Subject		Staff Name
1	EM-III	Engineering Mathematics-III	Mrs. D. Bhagya
	EE-II	Electronic Engineering-II	Mrs. E.V. Naga Lakshmi
2	PMP	Prime Movers & Pumps	Mr. B. Kshetra Mohan / Mr. S. Ramanathan
3	EC-I	Electrical Circuits-I	Mr. B. Ramakrishna
4	EMF	Electromagnetic Fields	Mr. VV. Satyanarayana Akula
5	DELD	Digital Electronics & Logic Design	Mr. P.V.V. Raghava Sharma
6	EVS	Environmental Sciences	Mr. A. Sanjay Kumar
7	EE LAB	Electronic Engineering Lab	Mrs. E.V. Naga Lakshmi/ Mr. M. Vijayakrishna
8	ME LAB	Mechanical Engineering Lab	Mr. R. Harnath / Mr. V. Suman Raju

Timetable coordinator

Chief Timetable Coordinator

HOD (EEE)

Principal

**MVSR ENGINEERING COLLEGE – NADERGUL**  
**Department of Electrical & Electronics Engineering**  
**Time Table for III/IV B.E. EEE (Sec-A) 2017-2018**

Room No: M-205

w.e.f. 16/08/2017

DAY	9.45 - 10.35	10.35 – 11.25	11.25 – 12.15	12.15 – 1.00	<b>LUNCH</b>	1.45 - 2.35	2.35 – 3.25	3.25 -4.15	
MON	PS-II	EM-II	LCS	LIC		<b>1.00-1.45</b>	PE	DELD	DELD
TUE	LCS	LIC	PS-II	PE			EM LAB-I (A) (KB/BRK/NSRK) / CS LAB (B) (GS/NBM)		
WED	EM LAB-I (B) (KB/BRK/NSRK) / CS LAB (C) (GS/VRK)			DELD			LCS	PS-II	
THU	PE	EM-II	DELD	LIC			EM LAB-I (C) (KB/BRK/NSRK) / CS LAB (A) (GS/VRK)		
FRI	LIC	LCS	PE	PS-II			EM-II	EM-II	
SAT	CRT	CRT	EM-II	LCS			PE		

A, B, C in parentheses indicate batch

**Legend:**

S. No.	Subject	Staff Name	
1	PS-II	Power Systems-II	Mr. S. Shyam Mohan
2	EM-II	Electrical Machinery-II	Dr. D.V.M. Chary
3	PE	Power Electronics	Mr. G. Satyanarayana
4	DELD	Digital Electronics and Logic Design	Mr. Y.L.N. Rao
5	LIC	Linear Integrated Circuits	Mr. V. Raghu Krishna
6	LCS	Linear Control Systems	Ms. G. Sirisha
7	EM-I LAB	Electrical Machines Lab - I	Dr. K. Brahmanandam / Mr. B. Ramakrishna / Mr. N. Siva Rama Krishna
8	CS LAB	Control Systems Lab	Ms. G. Sirisha / Mr. V. Raghukrishna / Mr. N. Bharath Mohan

**Timetable coordinator**

**Chief Timetable Coordinator**

**HOD (EEE)**

**Principal**

**MVSR ENGINEERING COLLEGE – NADERGUL**  
**Department of Electrical & Electronics Engineering**  
**Time Table for III/IV B.E. EEE (Sec-B) 2017-2018**

Room No: M-210

w.e.f. 16/08/2017

DAY	9.45 - 10.35	10.35 – 11.25	11.25 – 12.15	12.15 – 1.00	<b>LUNCH</b>	1.45 - 2.35	2.35 – 3.25	3.25 -4.15
<b>MON</b>	EM LAB-I (A) (GSN/INS) / CS LAB (B) (MRR/DHK)			EM-II		LCS	DELD	DELD
<b>TUE</b>	EM LAB-I (B) (GSN/INS) / CS LAB (C) (MRR/NR)			EM-II		LIC	PS-II	PS-II
<b>WED</b>		PE	PE	DELD		EM LAB-I (C) (GSN/INS) / CS LAB (A) (MRR/DHK)		
<b>THU</b>	LIC	PS-II	PE	EM-II		LCS	LCS	
<b>FRI</b>	PE	DELD	EM-II	PS-II		LCS	LIC	LIC
<b>SAT</b>	CRT	CRT	PE	LCS		EM-II		

A, B, C in parentheses indicate batch

**Legend:**

S. No.	Subject		Staff Name
1	PS-II	Power Systems-II	Dr. D. Harikrishna
2	EM-II	Electrical Machinery-II	Dr. K. Brahmanandam
3	PE	Power Electronics	Mr. G. Satyanarayana
4	DELD	Digital Electronics and Logic Design	Mr. P.V.V. Raghava Sharma
5	LIC	Linear Integrated Circuits	Mr. V. Raghu Krishna
6	LCS	Linear Control Systems	Mr. M. Raveendra Reddy
7	EM-I LAB	Electrical Machines Lab – I	Mr. G.Satyanarayana / Mr. I.N. Swamy
8	CS LAB	Control Systems Lab	Mr. M. Raveendra Reddy / Dr. D. Harikrishna / Mr. N. Ravi

**Timetable coordinator**

**Chief Timetable Coordinator**

**HOD (EEE)**

**Principal**

**MVSR ENGINEERING COLLEGE – NADERGUL**  
**Department of Electrical & Electronics Engineering**  
**Time Table for IV/IV B.E. EEE (Sec-A) 2017-2018**

Room No: M-102

w.e.f. 16/08/2017

DAY	9.45 - 10.35	10.35 – 11.25	11.25 – 12.15	12.15 – 1.00	<b>LUNCH</b> 1.00-1.45	1.45 - 2.35	2.35 – 3.25	3.25 -4.15
MON	Elective-I	Elective-I	PSOC	EMD		Project Seminar		
TUE	Elective-I	EMD	PSOC	EDSC		ES lab(A) (CVGK/VVS)/ MPMC lab(B) (PVVR/RSK) / PS lab(C) (SSM/NT)		
WED	---	PSOC	PSOC	Elective-I		EMD	EDSC	---
THU	ES lab(B) (CVGK/VVS) / MPMC lab(C) (PVVR/GS) / PS lab(A) (SSM/NT)			EDSC		---	---	---
FRI	PSOC	EMD	EDSC	---		ES lab(C) (CVGK/VVS) / MPMC lab(A) (PVVR/GS) / PS lab(B) (SSM/NT)		
SAT	CRT	CRT						

A, B, C in parentheses indicate batch

Legend:

SNo	Subject		Staff Name
1	PSOC	Power System Operation and Control	Mr. C.V.G.K. Rao
2	EDSC	Electric Drives and Static Control	Dr. T. Chandra Shekar
3	EMD	Electrical Machine Design	Mr. Y.L.N. Rao
4	Elective-I	HVDC / Entrepreneurship	Mr. P.B. Guru Prasanna / Mr. Ch. Venkata Rao
5	ES Lab	Electrical Simulation Lab	Mr. C.V.G.K. Rao / Mr. V.V. Satyanarayana Akula
6	MPMC Lab	Microprocessors and Microcontrollers Lab	Mr. P.V.V. Raghava Sharma / Ms. G. Sirisha / Mr. R. Sai Krishna
7	PS Lab	Power Systems Lab	Mr. S. Shyam Mohan / Ms. K. Navya Tejaswini
8	Seminar	Project Seminar	Mr. P.B. Guru Prasanna

**Timetable coordinator**

**Chief Timetable Coordinator**

**HOD (EEE)**

**Principal**

**MVSR ENGINEERING COLLEGE – NADERGUL**  
**Department of Electrical & Electronics Engineering**  
**Time Table for IV/IV B.E. EEE (Sec-B) 2017-2018**

Room No: M-103

w.e.f. 16/08/2017

DAY	9.45 - 10.35	10.35 – 11.25	11.25 – 12.15	12.15 – 1.00	<b>LUNCH</b> 1.00-1.45	1.45 - 2.35	2.35 – 3.25	3.25 -4.15
MON	Elective-I	Elective-I	EDSC	---		ES lab(A) (DHK/RSK) / MPMC lab(B) (TCS/BRK) / PS lab(C) (INS/NT)		
TUE	Elective-I	EDSC	EMD	PSOC		---	---	---
WED	ES lab(B) (DHK/RSK) / MPMC lab(C) (TCS/PVVR) / PS lab(A) (INS/NT)			Elective-I		---	---	---
THU	PSOC	EDSC	EMD	EMD		ES lab(C) (DHK/RSK) / MPMC lab(A) (TCS/PVVR) / PS lab(B) (INS/NT)		
FRI	EDSC	PSOC	PSOC	EMD		Project Seminar		
SAT	CRT	CRT						

A, B, C in parentheses indicate batch

Legend:

SNo	Subject		Staff Name
1	PSOC	Power System Operation and Control	Mr. I.N. Swamy
2	EDSC	Electric Drives and Static Control	Dr. T. Chandra Shekar
3	EMD	Electrical Machine Design	Mr. N. Siva Rama Krishna
4	Elective-I	HVDC / Entrepreneurship	Mr. P.B. Guru Prasanna / Mr. Ch. Venkata Rao
5	ES Lab	Electrical Simulation Lab	Dr. D. Harikrishna / Mr. R. Sai Krishna
6	MPMC Lab	Microprocessors and Microcontrollers Lab	Dr. T. Chandra Shekar/ Mr. P.V.V. Raghava Sharma / Mr. B. Ramakrishna
7	PS Lab	Power Systems Lab	Mr. I.N. Swamy/Ms. K. Navya Tejaswini
8	Seminar	Project Seminar	Dr. D. Harikrishna / Dr. T. Chandra Shekar

**Timetable coordinator**

**Chief Timetable Coordinator**

**HOD (EEE)**

**Principal**