

MVSR Engineering College
Electronics and Communication Department
LIST OF JOURNAL PAPERS PUBLISHED BY FACULTY
Acad. Year 2017-18

S. No	Name of the Staff	Title of the Paper	Name of Journal	ISSN NO & P.P NO, Vol.& Issue	Month & Year	National / International
1	Sudhir Dakey	"Design of Area and Power Efficient Line Decoders for SRAM"	International Journal of Emerging Technologies in Engineering Research (IJETER)	Volume 5, Issue 11, ISSN : 2454-6410, Pages : 51-56	November (2017)	International
2	E.V.Naga Lakshmi	Design and Implementation of Double Gate 8 T SRAM Cell Using MTCMOS	International Journal of Research	e-ISSN : 2348-6848 P-ISSN : 2348-795X Volume-4, Issue-10	Sep,2017	International
3	E.V.Naga Lakshmi	Design and Implementation of Double Gate SRAM Cells	International Journal of Research in Electronics & Communication Engineering - 2264	ISSN : 2321-5593(online) ISSN : 2321-032X(Print) Volume-5, Issue-5	Sep-Oct, 2017	International
4	Shilpa B Darvesh	Design and Implementation of Low Power LFSR For Fast ATPG Process	International Journal of Emerging Technologies in Engineering Research (IJETER)	ISSN : 2454-6410 Volume-5, Issue-9	Sep,2017	International
5	DVSR. Sesidhar M.Vijaya Krishna	Comparative Analysis of SWT Based OFDM and DWT based OFDM using Different Modulation Approaches to Improve BER Performance	International Journal of Scientific Engineering and Technology Research	ISSN -2319-8885 Vol-06, Issue-25	July,2017	International

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S. No	Name of the Staff	Title of the Paper	Name of Journal	ISSN NO & P.P NO, Vol.& Issue	Month & Year	National / International
1	V.SURESHKUMAR	Safe Driving without using mobile phones	International Journal for Innovative Engineering and Management Research	2456-5083 Vol-6, Issue 5, Page no.2198-2200	July, 2017	International
2	V.SURESHKUMAR	A High Capacity and Secured color image Stegnographic technique using Discrete Wavelet Transform	International Journal for Innovative Engineering and Management Research	2456-5083 Vol-6, Issue 5, Page no.2201-2209	July, 2017	International
3	T. Kavitha S.V.R.Manimala	Design of Fully Scalable Reconfigurable Parallel Architecture for the Computation of Approximate DCT	International Journal of Innovative computer Science and Engineering (IJICSE)	2393-8528 Vol-4, Issue-2, p.p no-126	March-April, 2017	International
4	N.Kavitha Sudhir Dakey	Removal of Artifacts Based on Weighted guided filtering for Digital Video quality	International Journal of Research	Vol-4, Issue-2,	Feb, 2017	International
5	N. Namassivaya	Presentation Analysis of Routing Protocols In Ad Hoc Network's And Its Portability	International Journal of Science, Technology and Management (IJSTM)	ISSN No-2250-0596, Vol-X, p.no-14-21,	Jan, 2017	International
6	B. Srinivas	Design and Analysis of Gate Diffusion input technology Techniques Based double gate Full Adder	International Journal of VLSI System Design and Communication Systems (IJVDCS)	Vol-4, Issue-12, (1384-1388) ISSN-23220929	Nov, 2016	International
7	N. Srinivas	Analytical Performance of Soft Data Fusion-Aided Spectrum Sensing in Hybrid Terrestrial-Satellite Networks	International Journal of Satellite Communications and Networking		Sep, 2016	International
8	S. Sreehari	FPGA Realization of MUX Based FIR Filter Architecture	International Journal of Engineering Science and Generic Research (IJESAR)	Vol 2, Issue 5, Pg no 64-68	Sep-oct, 2016	international
9	V.SURESHKUMAR	FPGA implementation of Vedic ALU with application specific Reversible Gates	International Journal of Engineering Science and Generic Reseach	2456-043X Vol-2, Issue 5, Page no.31-36	Sep-Oct, 2016	International
10	N. Namassivaya	Enhanced Low Power CMOS Current comparator using Diode Free Adiabatic (DFAL) Technique	International Journal of VLSI System Design and Communication Systems	ISSN-2322-0929 Vol-4, Issue-9	9 th Sep-2016	International

11	G.V.Chalapathi Rao	DEVELOPMENT OF A NEW EFFICIENT MULTIPATH MITTIGATION ALGORITHM FOR PRECISE GPS POSITION APPLICATIONS	THE IUP JOURNAL OF TELECOMMUNICATIONS	VOL III NO3	Aug,2016	International
12	E.V.Naga Lakshmi	Design of ALU Using Modified GDI Technology	International Journal of Advance Research (IJOAR)	ISSN- 2320-9186 Vol-4, Issue-8	Aug,2016	International
13	Shilpa B Darvesh	Enhanced Multi-threshold Schmitt Trigger Circuit Design Using FINFET Technology (IJVDCS)	International Journal	Vol-4, Issue-8	Aug,2016	International
14	T. Kavitha	A Low Power 32 Bit Mac Unit Design Using Vedic Multiplier and Brent-Kung Adder	International Journal of Engineering Science and Generic Research (IJESAR)	Vol-2, Issue-4, P.no-48-54	Aug,2016	International
15	N.SRINIVAS	Analytical performance of soft data fusion- aided spectrum sensing in hybrid terrestrial – satellite networks	International Journal of Satellite Communications and Networking	DOI:10.1002/sat.1200	Oct,2016	International

S. No	Name of the Staff	Title of the Paper	Name of Journal	ISSN NO & P.P NO, Vol.& Issue	Month & Year	National / International
16	M.Sarath Chandra	Design & Implementation of Thermometer Code TPC for BIST	International Journal (IJETR)	ISSN: 2348-2117 Vol-4, Issue-4, p.no-23-29	August, 2016	International
17	T. Kavitha	DWT Based Scheme For Video Watermarking	International Research Journal of Engineering and Technology (IRJET)	e-ISSN 2395-0056 p-ISSN- 2395-0072	2016	International
18	KVBL Deepthi	An investigation on Substrate Integrated Luneburg Lens Antenna with Gradient Index and Meta Materials	International Journal of applied engineering research (IJAER)	Print ISSN-0973-4562 Online ISSN-1087-1090	July,2016	International
19	E.V.Naga Lakshmi	Implementation and Comparison of Power Gated CMOS Circuits	International Research Journal of Engineering and Technology (IRJET)	e-ISSN- 2395-0056 p-ISSN- 2395-0072 Vol-3, Issue-7	July,2016	International
20	SVR Manimala	Designing of Avionics Full Duplex Switch on Netfpga	IOSR Journal Electronics and Communication Engineering	ISSN-2278-8735, Vol-11, Issue-4,Version-III P.No-17-27	July-Aug,2016	International
21	B. Bhavani	FPGA Based Performance Analysis of Micro programmed FIR filters using multipliers	International Journal of Engg. Science and Generic Research (IJESAR)	Vol-2, Issue : 4, P.no- 55-61	July-Aug,2016	International

22	B. Bhavani	LINUX Systems Development & Remote Debugging on Embedded Industrial Gate Way, IJJR	International Journal of Innovative Technology & Research)	Vol-4, Issue no-4, p.no: 3421-3129	June-July,2016	International
23	N. Namassivaya	Design of 32 Bit High Speed Six Stage Pipeline RISC Processor for Convolution	International Journal of Engineering Science and Generic Research (IJESAR)	ISSN-2456-043X, Journal Digital No- 05-2016-56583281 Journal DIDS Impact Factor-3.06- Vol-2, Issue-4	July-Aug,2016	International

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LIST OF JOURNAL PAPERS PUBLISHED BY FACULTY

Acad. Year 2015-16

S. No	Name of the Staff	Title of the Paper	Name of Journal	ISSN NO & P.P NO, Vol.& Issue	Month & Year	National / International
1	Shilpa B Darvesh T. Kavitha	FPGA Implementation of High Performance Fully Pipelined Aes Algorithm Using Reversible Logic	International Journal of Engineering Science and Generic Research (IJESAR)	ISSN : 2456-043X Vol. 2, Issue : 3, P.nos : 38-43	May-June, 2016	International
2`	E.V.Naga Lakshmi	A New Design of Reversible Full Subtractor	International Journal of Educational Research	ISSN: 2277-7881, Impact factor : 3.318: IC, Value: 5.16, ISI: Value: 2.286, Volume : 5, Issue :4, (5)	April, 2016	International
3	KVBL Deepthi	An Investigation of the Substrate-Integrated Luneburg Lens Antenna with Gradient Index and Meta-material Structures	International Journal of Applied Engineering Research	ISSN : 0973-4562, Vol-11, Number-8, p.p-5762-5766	April- May, 2016	International
4	Sudhir Dakey N.Kavitha	An Over view of Security Issues in Internet of Things	International Journal of Modern Trends in Engg. & Research	ISSN: 2349-9745 Vol-2, Issue-11	Nov,2015	International

5	Sudhir Dakey	Design and analysis of a Full Adder Using Various Reversible Gates	International Journal of Modern Trends in Engg. & Research	ISSN: 2349-9745 Vol-2, Issue-8	Aug,2015	International
6	Dr. G.Kanaka Durga	Design and implementation of Wallace Tree Multiplier Using Kogge Stone Adder and Brent	International Journal of Engineering Research & Technology	Vol-3, Issue-8, ppno-100-116, ISSN-2349-4395 (Print) ISSN-2349-4409 (online)	Aug,2015	International
7	Sudhir Dakey	Efficient Area and High Speed Advanced encryption Standard Algorithm	International Journal of Emerging Engineering Research and Technology	ISSN: 2349-4395, Vol-3, Issue -7	July,2015	International
8	N. Kavitha Dr. K. Usha	Optimized Evaluation Approach to Show the Timing Hitter and I/Q Imbalance Impact in OFDM Communication System	Global journal of Advanced Engineering Techniques	ISSN- (online) 2277-6370 & (Print) ISSN 2394-0921 Volume -4, Issue-4	2015	International
9	Sudhir Dakey	Design and Implementation of Area Delay Efficient Booth Multiplier Based on CBL	International Journal & Magazine of Engineering & Technology, Management and Research	ISSN NO-2320-3708 (Print) ISSN NO-8348-4845(Online)	July,2015	International
10	B. Sarala	Secure Data Communication using Cryptography and Steganography Standards	International Journal of Emerging Engineering Research and Technology	Vol.-3, Issue-7, p.p. no-129-139, ISSN 2349-4395 (Print) & ISSN 2349-4409(online)	July,2015	International
11	B. Sarala	Design and Analysis of Variable Body Bias SRAM	International Journal of Advanced Technology and Innovative Research	ISSN-2348-2370 Vol-07, Issue-06, p.p no-0852-0856	July,2015	International
13	B. Sarala	Fault Tolerant Architecture Design for Digital Adder	International Journal of Emerging Engineering Research and Technology	Vol-3, Issue-7, p.p no-41-47, ISSN-2349-4395(Print) & ISSN - 2349-4409 (online)	July,2015	International
14	E.V. Naga Lakshmi	Implementation of Parallel Prefix Adders Using Reversible Logic Gates	International Journal & Magazine of Engineering Technology, Management and Research	ISSN NO-2348-4845, Vol-2(2015), Issue no-7 (July) p.p. no-1631-1639	July,2015	International
15	B. Bhavani	Design and Implementation of Folded & Un-folded Tree Architectures for Processing Unit in Wireless Sensor Nodes	International Journal of Scientific Engineering and Technology Research	ISSN: 2319-8885, Vol-04, Issue-25, Pages 4895-4899	July,2015	International
14	S.V.R.Manimala	FPGA implementation of short duration Video Encryption & Decryption	IJESC	2321-3361	July,2015	International
15	V.SureshKumar	Convolution using Delay efficient improved hybrid multiplier	International Journal of Scientific Engineering and Technology	Vol.no.4, issue .26	July,2015	International
16	N.Srinivas	Cooperative Spectrum Sensing with Double Threshold and Censoring in Rayleigh Faded Cognitive Radio Network	Wireless Personal Communication	Vol.82,number.2	May,2015	International

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S. No	Name of the Staff	Title of the Paper	Name of Journal	ISSN NO & P.P NO, Vol.& Issue	Month & Year	National / International
1	N.Namassivaya	Dynamic Power Reduction in Frequency Divider Using Advanced Adiabatic	International Journal of Engineering & Science Research	ISSN : 2277-2685 VOL :-5/ Issue : 6/503-509	IJESR- June,2015	International
2	N. Kavitha	PARP reduction in OFDM system using non linear companding algorithm	International Journal of Scientific Engineering and Technology Research	ISSN : 2319-8885 Vol. no:4, p.p no 3491-3495 Issue 18	June 2015	International
3	Sudhir Dakey	Design, implementation and performance analysis of 8-bit vadic multiplier	International Journal of Modern Trends in Engg. & Research	Vol-2,Issue: 6, p.p no- 259-269	June,2015	International
4	Dr. N. Srinivas	Cooperative spectrum sensing with double threshold and censoring in Rayleigh faded cognitive radio network	International journal of Wireless personal communications, springer publication	Vol-84, issue-1 p.p no – 251-271	Sep,2015	International
5	Smitashree Mohopatra	Implementation of Carry select adder using CMOS full adders	IJERT	Vol-4, p.p no-5	2015	International
6	M. Vijay Krishna	Design of ternary sequence using of MATLAB	Journal of Advanced computing communication technology	ISSN-2347-2804 , Vol. no-3, issue 4,	Feb,2015	International
7	K.Usha	Performance analysis of new binary user codes for DSCDMA communication	Journal of Institution of Engineers (JIE) series- B	Vol-95, 40031-014-0163- 32250-2106	Dec.,2014	International
8	B. Srinivas	Design and implementation of sub microns level 10T full adder in ALU using cell based and SOC technology	International journals of Engineering research and applications	Vol-4, version-5, issue-9, ISSN-2248-9622 pp-147-150	Sep.,2014	International
9	E.V.Naga Lakshmi	Implementation of Time Efficient Architecture for Compressive Sensing Algorithm	International Journal of Multidisciplinary Education Research	ISSN-2277-7881, Impact factor -2735; IC Value-5- 16, Vol-3, Issue 9 (4)	Sep,2014	International

10	B.Bhavani	Embedded Memory Test and Repair	International Journal of Multi disciplinary Educational Research	Vol-3, ISSN no-10, pp-119-130	Oct,2014	International
11	S.Aruna	Implementation of Digital Serial FIR filter using wireless priority service (WPS).	International Journal of Advanced Research in Electronics and Communication Engineering	ISSN-2278-909X Vol-3, Issue-8 Pg 920-925	August, 2014	International
12	B. Sarala	Implementation of FIR filter using novel modulo adder for residue $2^n - 2^k - 1$ number system	International Organization of Scientific Research Community of Researcher	Vol-9, Issue-4,	July-Aug, 2014	International
13	B. Sarala	Design and implementation of Symmetric Cryptographic Algorithm using 5x5 square matrix with Single Point Crossover	International Journal of Advanced Research in Electronics and Communication Engineering	ISSN-2278-909X Vol-3, Issue-7 pp-745-754	July,2014	International
14	S.Aruna	A High resolution Low Offset Voltage Gain Stability Comparator	Progress in Science and Engineering Research Journal	Vol-2, ISSN-2347-6680	July,2014	International
15	S.Aruna	Multiplier less Design of Low Complexity FIR filter : Algorithms, CAD tools	International Journal of Advanced Research in Electronics and Communication Engineering	ISSN-2279-909X Vol-3, Issue-3	July,2014	International
16	KVBL Deepthi	A numerically efficient power optimization scheme for coded OFDM systems in achieving minimum frame error rate.	IJSRP	Volume-4, Issue-1	January-2014	International
17	KVBL Deepthi	A practical frame work for the performance evaluation of classical frequency planning schemes in OFDM based on Markov's model	IJERA	Volume-4, Issue-1 PP-341-345	January-2014	International