

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT

Timetable for B.E. III SEM (CBCS) ECE (SEC-A) Acad. Year: 2018-19- I Semester

Room No: EC-105

W.E.F: 10-09-2018

	9:30-10:30	10:30-11:30	11:30-12:30	12:30 1:15	1:15-2:15	2:15-3:15	3:15-4:15	
MON	M-III (Dr.NA)	EDLD/ EE LAB (MD/GSK/ SS, VRK)		L U N C H	ED (MD)	SATT (Dr. VCS)		
TUE	NAS (GVCR)	NAS (GVCR)	EME (SSK)		SATT (NK)	ED (MD)	M-III (Dr.NA)	
WED	STLD (Dr. BS)	STLD (Dr. BS)	AECES		NAS (GVCR)	EDLD/ EE LAB (MD/GSK/ SS, VRK)		
THU	EDLD/ EE LAB (MD/GSK/ SS, VRK)		STLD (Dr. BS)		M-III (Dr. NP)	EME (SSK)	ED (MD)	
FRI	ED (MD)	STLD (Dr. BS)	M-III (Dr. NP)		NAS (GVCR)	SATT (NK)	EME (SSK)	
SAT	AECES				ROBOTICS / e-LABS			

LEGEND:

S.N O	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	BS 301 MT	MATHEMATICS -III	Dr. NALINI PANYAM / Dr. A. NEELIMA	9866651433/ 9490020258
2	PC 302 EC	ELECTRONIC DEVICES	M. DEEPTHI	9885349485
3	PC 303 EC	SWITCHING THEORY & LOGIC DESIGN	Dr. B. SARALA	9502598968
4	PC 304 EC	SIGNAL ANALYSIS & TRANSFORM TECHNIQ.	Dr. V. CHANDRA SEK HAR/ N. KAVITHA	9966373428/ 9440147240
5	PC 305 EC	NETWORK ANALYSIS & SYNTHESIS	G.V. CHALAPATHI RAO	9848038209
6	ES 965 ME	ELEMENTS OF MECHANICAL ENGINEERING	S. SHIVA KUMAR	9177193944
PRACTICAL/LABORATORY COURSES				
7	PS 351 EC	ELECTRONIC DEVICES & LOGIC DESIGN LAB	M. DEEPTHI / G SRAVAN KUMAR/ SYVA SRINAVAS	9885349485 / 9030062090
8	PS 361 EE	ELECTRICAL ENGINEERING LAB	V. RAGHU KRISHNA/	9440653203

TIME TABLE CO-ORDINATORS**CHIEF TIME TABLE CO-ORDINATORS****HOD,ECE****MVSR ENGINEERING COLLEGE, ECE DEPARTMENT****Timetable for B.E. III SEM (CBCS) (SEC-B) Acad. Year: 2018-19- I Semester****Room No: EC-106****W.E.F: 10-09-2018**

	9:30-10:30	10:30-11:30	11:30-12:30	12:30:15	1:15-2:15	2:15-3:15	3:15-4:15
MON	STLD (KNR)	ED (Dr. SSR)	SATT (MVK)	L U N C H	EME (RH)		M-III (USR)
TUE	M-III (USR)	STLD (KNR)	NAS (MZA)		SATT (MVK)	ED (Dr. SSR)	
WED	EDLD/ EE LAB (KNR/KK/ MVK, NBM)		AECES		NAS (MZA)	STLD (KNR)	SATT (MVK)
THU	EME (RH)	SATT (MVK)	NAS (MZA)		M-III (USR)	EDLD/ EE LAB (KNR/KK/ MVK, NBM)	
Fri	M-III (USR)	STLD (KNR)	ED (Dr. SSR)		NAS (MZA)	EDLD/ EE LAB (KNR/KK/ MVK, NBM)	
SAT	AECES				ROBOTICS / e-LABS		

LEGEND:

S.N O	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	BS 301 MT	MATHEMATICS -III	U. SHANMUKHA RAO	9866059820
2	PC 302 EC	ELECTRONIC DEVICES	Dr. S. SWAPNA RANI	9573299074
3	PC 303 EC	SWITCHING THEORY & LOGIC DESIGN	K. NARSIMHA RAO	9440097517
4	PC 304 EC	SIGNAL ANALYSIS & TRANSFORM TECHNIQ.	M. VIJAY KRISHNA	8121512863
5	PC 305 EC	NETWORK ANALYSIS & SYNTHESIS	M. ZAHEER AHAMED	9177000358
6	ES 965 ME	ELEMENTS OF MECHANICAL ENGINEERING	R. HARANATH	9052381069

PRACTICAL/LABORATORY COURSES				
7	PS 351 EC	ELECTRONIC DEVICES & LOGIC DESIGN LAB	K. NARASIMHA RAO/ K.KAVYA/ M. VIJAY KRISHNA	9440097517 9542224602
8	PS 361 EE	ELECTRICAL ENGINEERING LAB	N. BHARATH MOHAN	9553208815

TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. III SEM (CBCS) (SEC-C) Acad. Year: 2018-19- I Semester

Room No: EC107

W.E.F: 10-09-2018

	9:30-10:30	10:30-11:30	11:30-12:30	12:30 1:15	1:15-2:15	2:15-3:15	3:15-4:15
MON	STLD (SS)	NAS (MZA)		L U N C H	M-III (MJ)	EDLD/ EE LAB (MZA/GSK/MSS, NR)	
TUE	STLD (SS)	NAS (MZA)	ED (GSK)		SATT (Dr, VCS)	EDLD/ EE LAB (MZA/GSK/MSS, NR)	
WED	SATT (Dr, VCS)		AECES		ED (GSK)	EME (RH)	
THU	M-III (MJ)	M-III (MJ)	STLD (SS)		NAS (MZA)	SATT (Dr, VCS)	ED (GSK)
FRI	EDLD/ EE LAB (MZA/GSK/MSS, NR)		(ED 44(GSK)		M-III (MJ)	EME (RH)	STLD (SS)
SAT	AECES					ROBOTICS / e-LABS	

LEGEND:

S.N O	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	BS 301 MT	MATHEMATICS -III	Mrs M. JYOTHSNA	9989979295
2	PC 302 EC	ELECTRONIC DEVICES	G. SRAVAN KUMAR	9030062090
3	PC 303 EC	SWITCHING THEORY & LOGIC DESIGN	Mr. SYVA SREENIVAS	9701279796
4	PC 304 EC	SIGNAL ANALYSIS & TRANSFORM	Dr. V. CHANDRA SEKHAR	9966373428

		TECHNIQ.		
5	PC 305 EC	NETWORK ANALYSIS & SYNTHESIS	M. ZAHEER AHAMED	9177000358
6	ES 965 ME	ELEMENTS OF MECHANICAL ENGINEERING	R. HARANATH	9052381069
PRACTICAL/LABORATORY COURSES				
7	PS 351 EC	ELECTRONIC DEVICES & LOGIC DESIGN LAB	M. ZAHEER AHAMED/ G SRAVAN KUMAR/ M. SAI SATYA SRI	9177000358/ 9030062090
8	PS 361 EE	ELECTRICAL ENGINEERING LAB	N. RAVI	9553505383

TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. V SEM (CBCS) (SEC-A) Acad. Year: 2018-19- I Semester

Room No: EC-111

W.E.F: 13-08-2018

	9:30-10:30	10:30-11:30	11:30-12:30	12:30 1:15	1:15-2:15	2:15-3:15	3:15-4:15
MON	COA (SRK)	DSD (SD)		L U N C H	DSP (BSR)	IC / SSP LAB (BS/GVCR, EVNL /SD,)	
TUE	LICA (EVNL)	COA (SRK)			DSD (SD)	IC / SSP LABHDL (BS/GVCR, EVNL /SD,)	
WED	ACS (NN)		LICA (EVNL)		DSP (BSR)	IC / SSP LAB (BS/GVCR, EVNL /SD,)	
THU	DSP (BSR)	LICA (EVNL)			AC (Dr. SPVMR)	GS (JS/GA)	
FRI	DSP (BSR)	AC (Dr. SPVMR)			ACS (NN)		COA (SRK)
SAT	GS (JS/GA)	MINI PROJECT			ROBOTICS / e-LABS		

LEGEND:

S.N	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
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THEORY COURSES				
1	PC 501 EC	LINAER INTEGRATED CIRCUITS & APPL.	E.V. NAGA LAKSHMI	9985818893
2	PC 502 EC	ANALOG COMMUNICATION	Dr. S.P. VENU MADHAVA RAO	9440493845
3	PC 503 EC	DIGITAL SIGNAL PROCESSING	B. SRINIVAS	9618028110
4	PC 504 EC	AUTOMATIC CONTROL SYSTEMS	N. NAMASSIVAYA	9866692214
5	PC 505 EC	COMPUTER ORGANIZATION & ARCHITECT.	S. RAVI KUMAR	9912479084
6	PC 506 EC	DSD USING VERIKOG HDL	SUDHIR DAKEY	9849958558
7		GENDER SENSITIZATION	JAI SHEELA/ G. ANAJENYELU	
PRACTICAL/LABORATORY COURSES				
7	PC 551 EC	IC APPLICATIONS LAB	EV NAGA LAKSHMI/ SUDHIR DAKEY	9985818893 9849958558
8	PC 552 EC	SIGNALS & SYSTEMS PROCESSING LAB	B. SRINIVAS/ G.V. CHALAPATHI RAO	9618028110 9848038209
9		MINI PROJECT	Dr B.SARALA, T.KAVITHA, DVSR SESIDHAR S.ARUNA, N.KAVITHA, S.RAVI KUMAR	

TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT

Timetable for B.E. V SEM (CBCS) (SEC-B) Acad. Year: 2018-19- I Semester

Room No: EC-113

W.E.F: 13-08-2018

	9:30-10:30	10:30-11:30	11:30-12:30	12:30 1:15	1:15-2:15	2:15-3:15	3:15-4:15
MON	ACS (GM)	AC (GVCR)	DSD (VRR)	L U N C H	LICA (Dr. SSR)	COA (SSH)	
TUE	LICA (Dr. SSR)	DSP (TK)	ACS (GM)		AC (GVCR)	DSD (VRR)	
WED	ACs (GM)	IC / SSP LAB (SSR/VRR, GM/ SSH,)			GS (KM/TK)	DSP (TK)	LICA (Dr. SSR)
THU	COA (SSH)	IC / SSP / Verilog HDL LAB (SSR/VRR, GM/SSH)			LICA (Dr. SSR)	DSP (TK)	

FRI	GS (KM/TK)	AC (GVCR)	COA (SSH)		ACS (GM)	IC / SSP LAB (SSR/VRR, GM/SSH,)
SAT	GS (KM/TK)	MINI PROJECT				ROBOTICS / e-LABS

LEGEND:

S.N O	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	PC 501 EC	LINAER INTEGRATED CIRCUITS & APPL.	Dr. S. SWAPNA RANI	9573299074
2	PC 502 EC	ANALOG COMMUNICATION	G. V. CHALAPATHI RAO	9848038209
3	PC 503 EC	DIGITAL SIGNAL PROCESSING	T. KAVITHA	7569110111
4	PC 504 EC	AUTOMATIC CONTROL SYSTEMS	G. MAHENDAR	9059364420
5	PC 505 EC	COMPUTER ORGANIZATION & ARCHITECT.	S. SREE HARI	9966924966
6	PC 506 EC	DSD USING VERIKOG HDL	V. RAJITHA RANI	9989674289
7		GENDER SENSITIZATION	K. MANJULA/ T. KRANTHI	9177163722
PRACTICAL/LABORATORY COURSES				
7	PC 551 EC	IC APPLICATIONS LAB	Dr. S. SWAPNA RANI / V. RAJITHA RANI	9573299074 9989674289
8	PC 552 EC	SIGNALS & SYSTEMS PROCESSING LAB	G. MAHENDAR / S. SREE HARI	9059364420 9966924966
9		MINI PROJECT	DR K.USHA, SHILPA B.DARVESH, B.SRINIVAS, B.BHAVANI, SUDHIR DHAKEY S.SREEHARI,	

TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. V SEM (CBCS) (SEC-C) Acad. Year: 2018-19- I Semester

Room No: EC116

W.E.F: 13-08-2018

	9:30-10:30	10:30-11:30	11:30-12:30	12:30 1:15	1:15-2:15	2:15-3:15	3:15-4:15
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MON	DSD (Dr. SPVMR)	LICA (NK)	GS (KM/GA)	L U N C H	AC (Dr. KU)		COA (VSK)
TUE	ACS (MSC)	IC(A) / SSP(B) LAB (NK/SS, SVRM /MSC)			DSP (SVRM)	DSP (SVRM)	LICA (NK)
WED	DSP (SVRM)	DSP (SVRM)	COA (VSK)		LICA (NK)	ACS (MSC)	
THU	AC (Dr. KU)	DSD (Dr. SPVMR)	COA (VSK)		ACS (MSC)	IC(B) / SSP(C) LAB (NK/SS, SVRM /MSC)	
FRI	LICA (NK)	IC(C) / SSP(A) LAB (NK/SS, SVRM /MSC)			COA (VSK)	DSD (Dr. SPVMR)	GS (KM/GA)
SAT	GS (KM/GA)	MINI PROJECT			ROBOTICS / e-LABS		

LEGEND:

S.N O	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	PC 501 EC	LINAER INTEGRATED CIRCUITS & APPL.	N. KAVITHA	9440147240
2	PC 502 EC	ANALOG COMMUNICATION	Dr. K. USHA	9989434423
3	PC 503 EC	DIGITAL SIGNAL PROCESSING	SVR MANIMALA	8885557272
4	PC 504 EC	AUTOMATIC CONTROL SYSTEMS	M. SHARATH CHANDRA	8341818901
5	PC 505 EC	COMPUTER ORGANIZATION & ARCHITECT.	V. SURESH KUMAR	9010150043
6	PC 506 EC	DSD USING VERIKOG HDL	Dr. S.P. VENU MADHAVA RAO	9440493845
7		GENDER SENSITIZATION	K. MANJULA/ G. ANJANEYULU	9177163722
PRACTICAL/LABORATORY COURSES				
7	PC 551 EC	IC APPLICATIONS LAB	N. KAVITHA / SYVA SRINIVAS	9440147240 9701279796
8	PC 552 EC	SIGNALS & SYSTEMS PROCESSING LAB	SVR MANIMALA / M. SHARATH CHANDRA	8885557272 8341818901
9		MINI PROJECT	NULI NAMASSIVAYA, S.V.R. MANIMALA, K.NARASIMHA RAO, Dr. S. SWAPNA RANI, V.RAJITHA RANI,/ M. ZAHEER AHAMED	

TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 4/4 ECE (SEC-A) Acad. Year: 2018-19- I Semester

Room No: EC115

W.E.F: 13-08-2018

	9:45-10:35	10:35-11:25	11:25-12:15	12:15-1:00	1:00-1:45	1:45-2:35	2:35-3:25	3:25-4:15
MON	ES/IOT/ ENTPS (BB-EC CH) / (DVSR-CE LAB) ENTPS-115	VLSI (SBD)	EI (NN)		L U N C H	MWE / VLSI &ES LAB (VVL/KR, SBD /SRK)		
TUE	MWE (VVL)	IAFM (GB)	DIP/OC (TK-EC 115) / (Dr.KU-EC CH)			MWE / VLSI &ES LAB (VVL/KR, SBD /SRK)		
WED	ES/IOT/ ENTPS (BB-EC CH) / (DVSR-CE LAB) ENTPS-115`		DIP/OC (TK-EC 115) / (Dr.KU-EC CH)			EI (NN)		
THU	VLSI (SBD)		MWE (VVL)	DIP/OC (TK-EC 115) / (Dr.KU-EC CH)		IAFM (GB)		
FRI	ES/IOT/ ENTPS (ES/IOT/ ENTPS (BB-EC CH) / (DVSR-107 ENTPS-115	MWE / VLSI &ES LAB (VVL/KR, SBD /SRK)				VLSI (SBD)	MWE (VVL)	
SAT	PROJECT SEMINAR					PROJECT WORK		

LEGEND:

S.NO	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	EC 401	MICROWAVE ENGINEERING	V. VIJAYA LAKSHMI	8686978170
2	EC 402	VLSI DESIGN	SHILPA B DARVESH	7799864497
3	EC 403	ELECTRONIC INSTRUMENTATION	N. NAMASSIVAYA	9866692214
4		IAFM	G. BHARATHI	9010299991
ELECTIVE I	EC 412	DIGITAL IMAGE PROCESING	T. KAVITHA	7569110111
	EC 411	OPTICAL COMMUNICATION	Dr. K. USHA	9989434423
ELECTIVE II	EC 421	EMBEDDED SYSTEMS	B. BHAVANI	9052502054
	EC 425	INTERNET OFTHINGS	DVSR SASIDHAR	9642871304
	ME 472	ENTERPRENUERSHIP	C. SUMALATHA	9441211177
PRACTICAL/LABORATORY COURSES				
7	EC 431	MICROWAVE ENGINEERING LAB	V VIJAYA LAKSHMI/ K. RAMINI	8686978170 7207714184
8	EC 432	EMBEDDED C & VLSI DESIGN LAB	SHILPA B DARVRSH / S RAVI KUMAR	7799864497 9912479084
9	EC 433	PROJECT SEMINAR	Dr. B SARALA/ N NAMASSIVAYA	9502598968 9866692214

TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT

Timetable for B.E. 4/4 ECE (SEC-B) Acad. Year: 2018-19- I Semester

Room No:

W.E.F: 13-08-2018

	9:45-10:35	10:35-11:25	11:25-12:15	12:15-1:00	1:00-1:45	1:45-2:35	2:35-3:25	3:25-4:15
MON	ES/IOT/ ENTPS (BB-EC CH) / (DVSR-CE LAB) ENTPS-115	MWE / VLSI &ES LAB (GM/VVL, SM / SSH)			L U N C H	IAFM (RN) (EC115)	EI (KK) (EC 115)	VLSI (SM)) (EC115)
TUE	EI (KK) (EC CH)	VLSI (SM) (EC CH)	DIP/OC (TK-EC 115) / (Dr.KU-EC CH)			MWE (KVBLD) (EC115)		IAFM (RN) (EC115)
WED	ES/IOT/ ENTPS (BB-EC CH) / (DVSR-106) ENTPS-115		DIP/OC (TK-EC 115) / (Dr.KU-EC CH)			MWE / VLSI &ES LAB (GM/VVL, SM / SSH)		
THU			EI (KK) (EC CH)	DIP/OC (TK-EC 115) / (Dr.KU-EC CH)		MWE / VLSI &ES LAB (GM/VVL, SM / SSH)		
FRI	ES/IOT/ ENTPS (BB-EC CH) / (DVSR-107) ENTPS-115	EI (KK) (EC 115)	MWE (KVBLD) (EC 115)			VLSI (SM) (EC CH)	IAFM (RN) (EC CH)	
SAT	PROJECT SEMINAR EI (KK)						PROJECT WORK	

LEGEND:

S.NO	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	EC 401	MICROWAVE ENGINEERING	KVBL DEEPTHI	9704093064
2	EC 402	VLSI DESIGN	SMITA SHREE M	9032680832
3	EC 403	ELECTRONIC INSTRUMENTATION	K KAVYA	9542224602
4		IAFM	S. RAMA NATHAN	9989717732
Elective-I	EC 412	DIGITAL IMAGE PROCESING	T. KAVITHA	7569110111
	EC 411	OPTICAL COMMUNICATION	Dr. K. USHA	9989434423
Elective-II	EC 421	EMBEDDED SYSTEMS	B. BHAVANI	9052502054
	EC 425	INTERNET OFTHINGS	DVSR SASIDHAR	9642871304
	ME 472	ENTERPRENUERSHIP	C. SUMALATHA	9441211177
PRACTICAL/LABORATORY COURSES				
7	EC 431	MICROWAVE ENGINEERING LAB	G MAHENDAR / V VIJAYA LAKSHMI	9059364420 8686978170
8	EC 432	EMBEDDED C & VLSI DESIGN LAB	SMITA SHREE M / S. SREE HARI	9032680832 9966924966
9	EC 433	PROJECT SEMINAR	Dr. K USHA	9989434423

			SVR MANIMALA	8885557272
TIME TABLE CO-ORDINATORS		CHIEF TIME TABLE CO-ORDINATORS		HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 4/4 ECE (SEC-C) Acad. Year: 2018-19- I Semester

Room No: CONFERENCE HALL

W.E.F: 13-08-2018

	9:45-10:35	10:35-11:25	11:25-12:15	12:15-1:00	1:00-1:45	1:45-2:35	2:35-3:25	3:25-4:15	
MON	ES/IOT/ ENTPS (BB-EC CH) / (DVSR-CE LAB) ENTPS-115	MWE (KVBLD)	MWE (KVBLD)	IAFM (RH)	L U N C H	VLSI (SA)		EI(GVKR)	
TUE	MWE / VLSI &ES LAB (KVBLD/ KD, SA / BB)			DIP/OC (TK-EC 115) / (Dr.KU-EC CH)		EI (GVKR)			
WED	ES/IOT/ ENTPS (BB-EC CH) /DVSR-106/ ENTPS-115		DIP/OC (TK-EC 115) / (Dr.KU-EC CH)			VLSI (SA)	EI (GVKR)	IAFM (RH)	
THU	MWE / VLSI &ES LAB (KVBLD/KD, SA / BB)			DIP/OC (TK-EC 115) / (Dr.KU-EC CH)		MWE (KVBLD)		EI (GVKR)	
FRI	ES/IOT/ ENTPS (ES/IOT/ ENTPS (BB-EC CH) / (DVSR-107 ENTPS-115	IAFM (RH)		VLSI (SA)		MWE / VLSI &ES LAB (KVBLD/KD, SA / BB)			
SAT	PROJECT SEMINAR					PROJECT WORK			

LEGEND:

S.NO	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	EC 401	MICROWAVE ENGINEERING	KVBL DEEPTHI	9704093064
2	EC 402	VLSI DESIGN	S. ARUNA	9704231417
3	EC 403	ELECTRONIC INSTRUMENTATION	G VISHNU KANTH REDDY	7995921465
4		IAFM	R. HARITHA	8121682017
Elec ti ve - I	EC 412	DIGITAL IMAGE PROCESING	T. KAVITHA	7569110111
	EC 411	OPTICAL COMMUNICATION	Dr. K. USHA	9989434423
Elec ti ve -II	EC 421	EMBEDDED SYSTEMS	B. BHAVANI	9052502054
	EC 425	INTERNET OFTHINGS	DVSR SASIDHAR	9642871304
	ME 472	ENTERPRENUERSHIP	C. SUMALATHA	9441211177
PRACTICAL/LABORATORY COURSES				
7	EC 431	MICROWAVE ENGINEERING LAB	KVBL DEEPTHI/ K. DIVYA	9704093064 9866077363
8	EC 432	EMBEDDED C & VLSI DESIGN LAB	B.BHAVANI /	9052502054

			S.ARUNA	9704231417
9	EC 433	PROJECT SEMINAR	Dr. V CHANDRA SEKHAR T KAVITHA	9966373428 7569110111

TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT

Timetable for M.E. 1/2 ECE (ES&VLSID) Acad. Year: 2018-19- I Semester

Room No: EC-108

W.E.F:

13.08.2018

TIME DAY	9:30-10:30	10:30-11:30	11:30-12:30	12:30-1:15	1:15-2:15	2:15-3:15	3:15-4:15
TUE	VLSIDT (SD)	ADDVH (Dr. BS)		LUNCH	D & S - I LAB (BB/ SA)		
WED	MESD (SRK)	DSP (BSR)	AICD (Dr. GKD)		VLSIDT (SD)	VPD (SBD)	
THU	ADDVH (Dr. BS)	MESD (SRK)			VLSIDT (SD)	DSP (BSR)	
FRI	VPD (SBD)	AICD (Dr. GKD)			SEMINAR - I (Dr. VCS/ BSR)		

LEGEND:

S.NO	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	EC 601	MICROCONTROLLERS FOR EMBDEDD SYS DESIGN	S. RAVI KUMAR	9912479084
2	EC 602	VLSI DESIGN & TECHNOLOGY	SUDHIR DAKEY	9849958558
3	EC 603	ANALOG IC DESIGN	Dr. G. KANAKA DURGA	9703804723
4	EC 606	VLSI PHYSICAL DESIGN	SHILPA B DARVESH	7799864497
5	EC 506	DIGITAL SIGNAL PROCESSORS	B. SRINIVAS	9618028110
6	EC 521	ADV. DIGITAL DESIGN WITH VERILOG HDL	Dr. B. SARALA	9502598968
PRACTICAL/LABORATORY COURSES				
7	EC607	Design and Simulation Laboratory-I	B. BHAVANI/ S. ARUNA	9052502054 9704231417

8	EC619	Seminar-I	Dr.V. CHANDRA SEKHAR / B. SRINIVAS	9966373428 9618028110
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TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE