

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 2/4 ECE (SEC-A) Acad. Year: 2017-18- I Semester

Room No: EC-105

W.E.F: 18-07-2017

	9.30 to 10.30	10.30 to 11.30	11.30 to 11.40	11.40 to 12.40	12.40 to 1.40	1.40 to 2.15	2.15 to 3.15	3.15 to 4.15
MONDAY	NT (GVCR)	M-III (Dr. AN)	B R E A K	SATT (Dr. TAK)	STLD (SRK)	L U N C H	ED (VRR)	EME (BKM)
TUESDAY	STLD (SRK)	M-III (Dr. AN)		ED (VRR)			EDLD/EE LAB (VRR,SRK,SBD) (A,B)	
WEDNESDAY	EDLD/EE LAB (VRR,SRK,SBD) (B,C)			EME (BKM)			ED (VRR)	NT (GVCR)
THURSDAY	SATT (Dr. TAK)			NT (GVCR)			STLD (SRK)	M-III (Dr. AN)
FRIDAY	M-III (Dr. AN)	STLD (SRK)		EDLD/EE LAB (VRR,SRK,NN) (C,A)			SATT (Dr. TAK)	
SATURDAY								

LEGEND:

Subject Code	Subject Name	Faculty Name
PC 301 EC	Electronic Devices	V. Rajitha Rani
PC 302 EC	Switching Theory and Logic Design	S. Ravi Kumar
PC 303 EC	Signal Analysis and Transform Techniques	Dr. T. Anil Kumar
PC 304 EC	Network Analysis and Synthesis	G.V.Chalapathi Rao
BS 305 MT	Engineering Mathematics -III	Dr. A. Neelima
MC 306 ME	Elements of Mechanical Engineering	B. Kshetra Mohan
PC 351 EC	Electronic Devices and Logic Design Lab	V. Rajitha Rani, S. Ravi Kumar, Shilpa B Darvesh,N. Namassivaya
ES 352 EE	Electrical Engineering Lab	Y.L.N Rao, M. Ravindra Reddy

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 2/4 ECE (SEC-B) Acad. Year: 2017-18- I Semester

Room No: EC-106

W.E.F: 18-07-2017

	9.30 to 10.30	10.30 to 11.30	11.30 to 11.40	11.40 to 12.40	12.40 to 1.40	1.40 to 2.15	2.15 to 3.15	3.15 to 4.15
MONDAY	STLD (VRR/ DVRS)		B R E A K	EDLD/ EE LAB (Dr. SSR, GVKR,CLS) (A,B)		L U N C H	NT (GVCR)	SATT (NK)
TUESDAY	NT (GVCR)			M-III (Dr.NP)			SATT (NK)	
WEDNESDAY	SATT (NK)			ED (Dr SSR)	EME (VNM)		EDLD/ EE LAB (Dr. SSR, GVKR,MZA) (B,C)	
THURSDAY	STLD (VRR / DVRS)			EME (VNM)			NT (GVCR)	ED (Dr. SSR)
FRIDAY	ED (Dr. SSR)			M-III (Dr.NP)			EDLD/ EE LAB (Dr. SSR, GVKR,CLS) (C,A)	
SATURDAY								

LEGEND:

Subject Code	Subject Name	Faculty Name
PC 301 EC	Electronic Devices	Dr. S. Swapna Rani
PC 302 EC	Switching Theory and Logic Design	V. Rajitha Rani, DVSR. Sesidhar
PC 303 EC	Signal Analysis and Transform Techniques	N. Kavitha
PC 304 EC	Network Analysis and Synthesis	G.V.Chalapathi Rao
BS 305 MT	Engineering Mathematics -III	Dr. Nalini Panyam
MC 306 ME	Elements of Mechanical Engineering	V. Nikhil Murthy
PC 351 EC	Electronic Devices and Logic Design Lab	Dr. S. Swapna Rani, G. Vishnukanth Reddy, C. Laxmana Sai, M. Zaheer Ahmed
ES 352 EE	Electrical Engineering Lab	R. Sai Krishna, N. Siva Rama Krishna

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 2/4 ECE (SEC-C) Acad. Year: 2017-18- I Semester

Room No: EC-107

W.E.F: 18-07-2017

	9.30 to 10.30	10.30 to 11.30	11.30 to 11.40	11.40 to 12.40	12.40 to 1.40	1.40 to 2.15	2.15 to 3.15	3.15 to 4.15
MONDAY	EDLD/ EE LAB (MD,MZA,GVKR) (A,B)		B R E A K	EME (RHN)		L U N C H	STLD (SS)	NT (MZA)
TUESDAY	M-III (PS)	ED (MD)		EDLD/ EE LAB (MD,MZA,GM) (B.C)			EME (RHN)	SATT (GISR)
WEDNESDAY	NT (MZA)			SATT (GISR)	ED (MD)		M-III (PS)	STLD (SS)
THURSDAY	STLD (SS)			ED (MD)	M-III (PS)		EDLD/ EE LAB (MD,MZA,GM) (C,A)	
FRIDAY	NT (MZA)	SATT (Dr. VCS)		SATT (Dr. VCS)	ED (MD)		M-III (PS)	
SATURDAY								

LEGEND:

Subject Code	Subject Name	Faculty Name
PC 301 EC	Electronic Devices	M.Deepthi
PC 302 EC	Switching Theory and Logic Design	Syva Srinias
PC 303 EC	Signal Analysis and Transform Techniques	Dr. V. Chandra Sekhar, Indrasena Reddy
PC 304 EC	Network Analysis and Synthesis	M. Zaheer Ahmed
BS 305 MT	Engineering Mathematics -III	P.Swapna
MC 306 ME	Elements of Mechanical Engineering	R. Haranath
PC 351 EC	Electronic Devices and Logic Design Lab	M. Deepthi, M. Zaheer Ahmed, G. Mahendar, G. Vishnukanth Reddy
ES 352 EE	Electrical Engineering Lab	VV Satyanarayana.A , Y. C. N Rao

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 3/4 ECE (SEC-A) Acad. Year: 2017-18- I Semester

Room No: EC-111

W.E.F: 18-07-2017

	9.45 to 10.35	10.35 to 11.25	11.25 to 12.15	12.15 to 1.00	1.00 to 1.45	1.45 to 2.35	2.35 to 3.25	3.25 to 4.15
MONDAY	COA (Dr. BS)	PDIC / VERILOG HDL LAB (GVCR,BSR) (TK, SD) (A,B)			L U N C H	DSD (SD)	LICA (MVK)	
TUESDAY	ACS (GSK)		PDC (BSR)			PDIC / VERILOG HDL LAB (GVCR,BSR) (Dr. BS, SD) (B,C)		
WEDNESDAY	COA (Dr. BS)	PDIC / VERILOG HDL LAB (GVCR,BSR) (Dr. BS, SD) (C,A)				DSD (SD)	PDC (BSR)	
THURSDAY	LICA (MVK)		AC (GM)			ACS (GSK)		
FRIDAY	COA (Dr. BS)		DSD (SD)			AC (GM)		
SATURDAY								

LEGEND:

Subject Code	Subject Name	Faculty Name
EC 301	Linear IC's and Application	M.Vijaya Krishna
EC 302	Pulse and Digital Circuits	B. Srinivas
EC 303	Analog Communication	G. Mahendar
EC 304	Automatic Control Systems	G. Sravan Kumar
EC 305	Computer Organization and Architecture	Dr. B. Sarala
EC 306	Digital System Design with VERILOG HDL	Sudhir Dakey
EC 331	Pulse, Digital and Integrated Circuits Lab	G.V.Chalapathi Rao, B. Srinivas
EC 332	VERILOG HDL Lab	Dr. B. Sarala, Sudhir Dakey ,T. Kavitha

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT

Timetable for B.E. 3/4 ECE (SEC-B) Acad. Year: 2017-18- I Semester

Room No: EC-113

W.E.F: 18-07-2017

	9.45 to 10.35	10.35 to 11.25	11.25 to 12.15	12.15 to 1.00	1.00 to 1.45	1.45 to 2.35	2.35 to 3.25	3.25 to 4.15
MONDAY	LICA (NK)		DSD (SM)		L U N C H	COA (TK)	AC (SVRM)	
TUESDAY	PDIC / VERILOG HDL LAB (SS,NK) (SVRM, SM) (A,B)					PDC (Dr. SSR)		
WEDNESDAY	PDC (SS)		DSD (SM)			COA (TK)	ACS (NN)	
THURSDAY	COA (TK)		ACS (NN)			PDIC / VERILOG HDL LAB (SS,NK) (SVRM, SM) (B,C)		
FRIDAY	AC (SVRM)		LICA (NK)			PDIC / VERILOG HDL LAB (SS,NK) (SVRM, SM) (C,A)		
SATURDAY								

LEGEND:

Subject Code	Subject Name	Faculty Name
EC 301	Linear IC's and Application	N.Kavitha
EC 302	Pulse and Digital Circuits	Dr. S. Swapna Rani, Syva Srinivas
EC 303	Analog Communication	SVR. Manimala
EC 304	Automatic Control Systems	N. Namassivaya
EC 305	Computer Organization and Architecture	T.Kavitha
EC 306	Digital System Design with VERILOG HDL	Smitashree M
EC 331	Pulse, Digital and Integrated Circuits Lab	Syva Srinivas, N. Kavitha
EC 332	VERILOG HDL Lab	SVR. Manimala, Smitashree M

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 3/4 ECE (SEC-C) Acad. Year: 2017-18- I Semester
Room No: EC-116 **W.E.F: 18-07-2017**

	9.45 to 10.35	10.35 to 11.25	11.25 to 12.15	12.15 to 1.00	1.00 to 1.45	1.45 to 2.35	2.35 to 3.25	3.25 to 4.15
MONDAY	DSD (SSH)		ACS (NN)		L U N C H	PDIC / VERILOG HDL LAB (KNR, SA) (SSH, GM) (A,B)		
TUESDAY	COA (BB)		AC (Dr. TAK)			DSD (SSH)		
WEDNESDAY	LICA (SA)		AC (Dr. VCS)			PDIC / VERILOG HDL LAB (KNR, SA) (SSH, GM) (B.C)		
THURSDAY	COA (BB)		PDC (MZA)			LICA (SA)	ACS (NN)	
FRIDAY		PDIC / VERILOG HDL LAB (KNR, SA) (SSH, GM) (C,A)				LICA (SA)	PDC (MZA)	
SATURDAY								

LEGEND:

Subject Code	Subject Name	Faculty Name
EC 301	Linear IC's and Application	S. Aruna
EC 302	Pulse and Digital Circuits	M.Zaheer Ahmed
EC 303	Analog Communication	Dr. V. Chandra Sekhar, Dr. T. Anil Kumar
EC 304	Automatic Control Systems	N. Namassivaya
EC 305	Computer Organization and Architecture	B. Bhavani
EC 306	Digital System Design with VERILOG HDL	S. Sreehari
EC 331	Pulse, Digital and Integrated Circuits Lab	K. Narasimha Rao, S. Aruna
EC 332	VERILOG HDL Lab	S. Sreehari, G. Mahendar

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 4/4 ECE (SEC-A) Acad. Year: 2017-18- I Semester

Room Nos : CH / EC-111/ EC-113 / EC-115 / EC-116

W.E.F: 18-07-2017

	9.45 to 10.35	10.35 to 11.25	11.25 to 12.15	12.15 to 1.00	1.00 to 1.45	1.45 to 2.35	2.35 to 3.25	3.25 to 4.15
MONDAY	MWE LAB / EC &VLSI LAB (VVL, GISR) (SBD, SRK) (A,B)			ES/ ENTPS (BB/GB) Elective-II (R.N-CH / EC-115, 111)	L U N C H	IAFM (GSR) (EC-116)	VLSID(SBD) (EC-116)	
TUESDAY	MWE (VVL) (CH)	VLSID (SBD) (CH)	DIP/ OFC (TK/ KNR) Elective-I (R.N- EC-115, CH)	EI (GVKR) (CH)		IAFM (GSR) (CH)		
WEDNESDAY	DIP/ OFC/ FPGA (TK/ KNR/X) Elective-I (R.N- EC-115, CH)		ES/ENTPS (BB/GB) Elective-II (R.NO-CH, EC-111,115)			MWE LAB / EC &VLSI LAB (VVL, GISR) (SBD, SRK)(B,C)		
THURSDAY	MWE LAB / EC &VLSI LAB (VVL, GISR) (SBD, SRK) (C,A)			DIP/ OFC/ FPGA (TK/ KNR/X) Elective-I (R.N- EC-115, CH)		EI (GVKR) (EC-113)	MWE (VVL) (EC-113)	
FRIDAY	EI (GVKR) (CH)		VLSID (SBD) (CH)	ES/ENTPS (BB/GB) Elective-II (R.N-CH / EC-115, 116)		IAFM (GSR) (CH)		

LEGEND:

Subject Code	Subject Name	Faculty Name
EC 401	Microwave Engineering	V. Vijaya Lakshmi
EC 402	VLSI Design	Shilpa B Darvesh
EC 403	Electronic Instrumentation	G.Vishnukanth Reddy
Elective-I		
EC 411	Optical Fibre Communication	K. Narasimha Rao
EC 412	Digital Image Processing	T.Kavitha
Elective-II		
EC 421	Embedded Systems	B.Bhavani
ME XXX	Entrepreneurship	G. Bharathi
	Industrial Administration and Financial Management	G. Srinivasa Rao
EC 431	Microwave Lab	V.Vijaya Lakshmi, G. Indrasena Reddy
EC 432	Embedded C and VLSI Design Lab	Shilpa B Darvesh, S. Ravi Kumar
EC 433	Project Seminar	Dr. G. Kanaka Durga, SVR. Manimala

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT

Timetable for B.E. 4/4 ECE (SEC-B) Acad. Year: 2017-18- I Semester

Room Nos : CH / EC-111/ EC-113 / EC-115 / EC-116

W.E.F: 18-07-2017

	9.45 to 10.35	10.35 to 11.25	11.25 to 12.15	12.15 to 1.00	1.00 to 1.45	1.45 to 2.35	2.35 to 3.25	3.25 to 4.15
MONDAY	MWE (KVBLD) (CH)		VLSID (SA) (CH)	ES/ ENTPS (BB/SEP)Elective-II (R.N-CE / EC-115, 111)	L U N C H	EI (DVSR/GISR) (CH)		
TUESDAY	MWE LAB / EC &VLSI LAB (EVNL, MVK, CLS) (SSH, DVSRs, VRR) (A,B)			DIP/ OFC (TK/ KNR)Elective-I (R.N- EC-115, CH)		VLSID (SA) (EC-111)	IAFM () (EC-111)	
WEDNESDAY	DIP/ OFC (TK/ KNR)Elective-I (R.N- EC-115, CH)		ES/ ENTPS (BB/SEP)Elective-II (R.N-CH / EC-115, 111)			MWE (KVBLD)		
THURSDAY	VLSID (SA) (CH)		IAFM () (CH)	DIP/ OFC (TK/ KNR)Elective-I (R.N- EC-115, CH)		MWE LAB / EC &VLSI LAB (EVNL, MVK) (SSH, DVSRs, VRR) (B,C)		
FRIDAY	EI (DVSR/GISR) (EC-115)		IAFM () (EC-115)	ES/ ENTPS (BB/SEP)Elective-II (R.N-CH / EC-115, 116)		MWE LAB / EC &VLSI LAB (EVNL, MVK) (SSH, DVSRs, VRR) (C,A)		

Subject Code	Subject Name	Faculty Name
EC 401	Microwave Engineering	KVBL. Deepthi
EC 402	VLSI Design	S.Aruna
EC 403	Electronic Instrumentation	DVSR Sesidhar , G. Indrasena Reddy
Elective-I		
EC 411	Optical Fibre Communication	K.Narasimha Rao
EC 412	Digital Image Processing	T.Kavitha
Elective-II		
EC 421	Embedded Systems	B. Bhavani
ME XXX	Entrepreneurship	S.E.Praveena
	Industrial Administration and Financial Management	
EC 431	Microwave Lab	E.V.Naga Lakshmi, M. Vijaya Krishna, C. Laxmana Sai
EC 432	Embedded C and VLSI Design Lab	S. Sreehari, DVSR. Sesidhar , V. Rajitha Rani
EC 433	Project Seminar	Dr. T. Anil Kumar , T. Kavitha

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for B.E. 4/4 ECE (SEC-C) Acad. Year: 2017-18- I Semester

Room Nos : CH / EC-111/ EC-113 / EC-115 / EC-116

W.E.F: 18-07-2017

	9.45 to 10.35	10.35 to 11.25	11.25 to 12.15	12.15 to 1.00	1.00 to 1.45	1.45 to 2.35	2.35 to 3.25	3.25 to 4.15
MONDAY	VLSID (Dr. GKD) (EC-115)		IAFM (GSR) (EC-115)	ES/ ENTPS (BB/GB)Elective-II (R.N-CH / EC-115, 111)	L U N C H	MWE LAB / EC &VLSI LAB (KVBL, VVL) (BB, SM) (A,B)		
TUESDAY	EI (GISR) (EC-115)		IAFM (GSR) (EC-115)	DIP/ OFC (TK/ KNR)Elective-I (R.N- EC-115, CH)		MWE LAB / EC &VLSI LAB (KVBL, VVL) (BB, SM) (B,C)		
WEDNESDAY	DIP/ OFC (TK/ KNR)Elective-I (R.N- EC-115, CH)		ES/ ENTPS (BB/GB)Elective-II (R.N-CH / EC-115, 111)			VLSID (Dr. GKD) (EC-115)	IAFM (GSR) (EC-115)	
THURSDAY	MWE (KVBLD) (EC-115)		VLSID (Dr. GKD) (EC-115)	DIP/ OFC (TK/ KNR)Elective-I (R.N- EC-115, CH)		EI (GISR) (EC-115)		
FRIDAY	MWE LAB / EC &VLSI LAB (KVBL, VVL) (BB, SM) (C,A)			ES/ ENTPS (BB/GB)Elective-II (R.N-CH / EC-115, 116)		MWE (KVBLD) (EC-113)		
SATURDAY								

LEGEND:

Subject Code	Subject Name	Faculty Name
EC 401	Microwave Engineering	KVBL. Deepthi
EC 402	VLSI Design	Dr. G. Kanaka Durga
EC 403	Electronic Instrumentation	G.Indrasena Reddy
Elective-I		
EC 411	Optical Fibre Communication	K.Narasimha Rao
EC 412	Digital Image Processing	T.Kavitha
Elective-II		
EC 421	Embedded Systems	B.Bhavani
ME XXX	Entrepreneurship	G.Bharathi
	Industrial Administration and Financial Management	G.Srinivasa Rao
EC 431	Microwave Lab	KVBL Deepthi, V. Vijaya Lakshmi
EC 432	Embedded C and VLSI Design Lab	B.Bhavani, Smitashree M
EC 433	Project Seminar	Dr. B. Sarala , B. Srinivas, Dr. S. Swapna Rani