

**MVSR ENGINEERING COLLEGE, ECE DEPARTMENT**  
**Timetable for B.E. 2/4 ECE (SEC-A) Acad. Year: 2016-17- I Semester**

Room No: EC-105

W.E.F: 08-08-2016

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15
MONDAY		EME (RHN)		BCA (DVSRS)		L U N C H	M-III (KRB)	EMT (GM)	
TUESDAY		M-III (KRB)		EMT (GM)			ED (VRR)	EME (RHN)	
WEDNESDAY		ET (PBGP)		ED (VRR)			ED LAB/ EWS LAB (A/B) (NK/VRR) / (NN/ NS)		
THURSDAY		ED (VRR)	ED LAB/ EWS LAB (B/C) (NK/VRR) / (NN/ NS)				BCA (DVSRS)	ET (PBGP)	
FRIDAY		BCA (DVSRS)	ED LAB/ EWS LAB (C/A) (NK/VRR) / (NN/ NS)				CRT		M-III (KRB)

**LEGEND:**

Subject Code: Subject Name	Faculty Name
MAT-202 : Applied Mathematics	Dr. K. Ramesh Babu
EC- 201 : Basic Circuit Analysis	DVSR. Sesidhar
EC-202 : Electromagnetic Theory	G. Mahendar
EC-203 : Electronic Devices	V. Rajitha Rani
ME-221 : Elements of Mechanical Engineering	R. Haranath
EE-222 : Electrical Technology	P.B. Guru Prasanna
EC-231 : Electronic Devices Lab	N. Kavitha/ V. Rajitha Rani
EC-232 : Electronic Workshop and Simulation Lab	N. Namassivaya/ Dr. N. Srinivas

Timetable Co-ordinators.

Chief TT Co-ordinator

HECE

Principal

**MVSR ENGINEERING COLLEGE, ECE DEPARTMENT**  
**Timetable for B.E. 2/4 ECE (SEC-B) Acad. Year: 2016-17- I Semester**

Room No: EC-106

W.E.F: 08-08-2016

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15
MONDAY		EME (RRK)	EMT (AVD)		ED (SSR)	L U N C H	ED LAB/ EWS LAB (A/B) (SSR/ MD) / (DVSRS/ SM)		
TUESDAY		BCA (DVSRS)		M-III (NP)			ET (YLNR)	EMT (AVD)	
WEDNESDAY		ED (SSR)	ED LAB/ EWS LAB (B/C) (SSR/ MD) / (DVSRS)				EME (RRK)	ET (YLNR)	
THURSDAY		ED (SSR)		EME (RRK)			ET (YLNR)	M-III (KRB)	
FRIDAY		CRT		BCA (DVSRS)			ED LAB/ EWS LAB (C/A) (SSR/ MD) / (DVSRS/ GVCR)		

**LEGEND:**

Subject Code: Subject Name	Faculty Name
MAT-202 : Applied Mathematics	Dr. Nalini Panyam/ Dr. K. Ramesh Babu
EC- 201 : Basic Circuit Analysis	DVSR. Sesidhar
EC-202 : Electromagnetic Theory	Avinash Dasi
EC-203 : Electronic Devices	Dr. S. Swapna Rani
ME-221 : Elements of Mechanical Engineering	R. Ravi Kumar
EE-222 : Electrical Technology	Y.L.N. Rao
EC-231 : Electronic Devices Lab	Dr. S. Swapna Rani / M. Deepthi
EC-232 : Electronic Workshop and Simulation Lab	DVSR. Sesidhar/ G.V.Chalpathi Rao (1) Smitashree M (1)

Timetable Co-ordinators.

Chief TT Co-ordinator

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Principal

**Timetable for B.E. 2/4 ECE (SEC-C) Acad. Year: 2016-17- I Semester**

Room No: EC-107

W.E.F: 08-08-2016

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15
MONDAY		ED (TAK)	ED LAB/ EWS LAB (A/B) (TAK/ KNR) / (GM/ GSK)			L	ET (SSM)	M-III (PS)	
TUESDAY		M-III (PS)	ET (SSM)	ED (TAK)		U	ED LAB/ EWS LAB (B/C) (TAK/ KNR) / (GM/ SD)		
WEDNESDAY		EMT (GM)		BCA (VCS)		N	M-III (PS)	EME (RHN)	
THURSDAY		ET (SSM)		BCA (VCS)		C	ED LAB/ EWS LAB (C/A) (TAK/ KNR) / GM/ SD		
FRIDAY		EME (RHN)		CRT		H	ED (TAK)	EMT (GM)	

**LEGEND:**

Subject Code: Subject Name	Faculty Name
MAT-202 : Applied Mathematics	P. Swapna
EC- 201 : Basic Circuit Analysis	Dr. V. Chandra Sekhar
EC-202 : Electromagnetic Theory	G. Mahendar
EC-203 : Electronic Devices	Dr. T. Anil Kumar
ME-221 : Elements of Mechanical Engineering	R. Harnath
EE-222 : Electrical Technology	S. Shyam Mohan
EC-231 : Electronic Devices Lab	Dr. T. Anil Kumar / K. Narasimha Rao
EC-232 : Electronic Workshop and Simulation Lab	G. Mahendar / G. Sravan Kumar (1)/ Sudhir Dakey (2)

**Timetable Co-ordinators.**

**Chief TT Co-ordinator**

**HECE**

**Principal**

**Timetable for B.E. 3/4 ECE (SEC-A) Acad. Year: 2016-17- I Semester**

Room No: EC-111

W.E.F: 08-08-2016

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15
MONDAY		AC (GVCR)	COA (BB)	ACS (NN)		L	PDC (BSR)	LICA (EVNL)	
TUESDAY		LICA (EVNL)		COA (BB)		U	PDIC LAB/ VERILOG HDL LAB (A/B) (BSR/ MVK) / (BB/ SSH)		
WEDNESDAY		ACS (NN)		AC (GVCR)		N	COA (BB)	DSD (SSH)	
THURSDAY		PDC (BSR)		DSD (SSH)		C	PDIC LAB/ VERILOG HDL LAB (B/C) (BSR/ MVK) / (BB)		
FRIDAY		AC (GVCR)	PDIC LAB/ VERILOG HDL LAB(C/A) (BSR/ MVK) / (BB/ SSH)			H	CRT		PDC (BSR)

**LEGEND:**

Subject Code: Subject Name	Faculty Name
Analog Communication	GV. Chalapathi Rao
Pulse and Digital circuits	B. Srinivas
Linear Integrated Circuits	E.V. Naga Lakshmi
Digital System Design with VERILOG HDL	S. Sreehari
Computer Organization and Architecture	B. Bhavani
Automatic Control Systems	N. Namassivaya
Pulse, Digital and Integrated Circuits Lab	B. Srinvas / M. Vijaya Krishna
Verilog HDL Lab	B. Bhavani/ S. Sreehari (2)
Industrial Visit	DVSR. Sesidhar/ E.V. Naga Lakshmi

Timetable Co-ordinators.

Chief TT Co-ordinator

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Principal

**Timetable for B.E. 3/4 ECE (SEC-B) Acad. Year: 2016-17- I Semester**

Room No: EC-113

W.E.F: 08-08-2016

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15
MONDAY		COA (TK)	PDIC LAB/ VERILOG HDL LAB(A/B) (SA/ MVK) / (SM/TK)			L U N C H	LICA (SA)	AC (MVK)	
TUESDAY		DSD (SM)		AC (MVK)			LICA (SA)	COA (TK)	
WEDNESDAY		PDC (SS)	LICA (SA)		COA (TK)		PDIC LAB/ VERILOG HDL LAB(B/C) (SA/ MVK) / (SM/TK)		
THURSDAY		PDC (SS)	PDIC LAB/ VERILOG HDL LAB(C/A) (SA/ MVK) / (SM/TK)				DSD (SM)	ACS (GSK)	
FRIDAY		CRT		ACS (GSK)			DSD (SM)	PDC (SS)	

**LEGEND:**

Subject Code: Subject Name	Faculty Name
Analog Communication	M. Vijaya Krishna
Pulse and Digital circuits	Syva Srinivas
Linear Integrated Circuits	S. Aruna
Digital System Design with VERILOG HDL	Smitashree M
Computer Organization and Architecture	T.Kavitha
Automatic Control Systems	G. Sravan Kumar
Pulse, Digital and Integrated Circuits Lab	S. Aruna/ M. Vijaya Krishna
Verilog HDL Lab	Smitashree M/ T. Kavitha
Industrial Visit	N. Namassivaya/ KVBL. Deepthi

Timetable Co-ordinators.

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**MVSR ENGINEERING COLLEGE, ECE DEPARTMENT**  
**Timetable for B.E. 3/4 ECE (SEC-C) Acad. Year: 2016-17- I Semester**

Room No: EC-116

W.E.F: 08-08-2016

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15
MONDAY		COA (BS)		LICA (NK)		L U N C H	PDIC LAB/ VERILOG HDL LAB(A/B) (NK) / (BS/ SSH)		
TUESDAY		COA (BS)	ACS (MSC)	DSD (SSH)			LICA (NK)	PDC (GVCR)	
WEDNESDAY		LICA (NK)	PDIC LAB/ VERILOG HDL LAB(B/C) (NK/ EVNL) / (BS/ SSH)				ACS (MSC)	AC (TAK)	
THURSDAY		PDC (GVCR)		ACS (MSC)			COA (BS)	DSD (SSH)	
FRIDAY		AC (TAK)		CRT			PDIC LAB/ VERILOG HDL LAB (C/A) (NK/ EVNL) / (BS/ SSH)		

**LEGEND:**

Subject Code: Subject Name	Faculty Name
Analog Communication	Dr. T. Anil Kumar
Pulse and Digital circuits	G.V. Chalapathi Rao
Linear Integrated Circuits	N. Kavitha
Digital System Design with VERILOG HDL	S. Sreehari
Computer Organization and Architecture	B. Sarala
Automatic Control Systems	M. Sarath Chandra
Pulse, Digital and Integrated Circuits Lab	N. Kavitha/ E.V. Naga Lakshmi (2)
Verilog HDL Lab	B. Sarala/ S. Sreehari
Industrial Visit	G.V. Chalapathi Rao/ Shilpa B Darvesh

Timetable Co-ordinators.

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Principal

**Timetable for B.E. 4/4 ECE (SEC-A) Acad. Year: 2016-17- I Semester  
W.E.F: 08-08-2016**

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15
MONDAY		IAFM (GSR) R.NO- (EC-115)		VLSI (SD) R.NO- (EC-115)		L U N C H	CN (SVRM) R.NO- (EC-115)	DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	
TUESDAY		MWE (AVD) R.NO- (EC-115)		MCC (NS) R.NO- (EC-115)			IAFM (GSR) R.NO- (EC-115)	CN (SVRM) R.NO- (EC-115)	
WEDNESDAY		DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	MW LAB/ EDA LAB (A/B) (AVD) / (SD)				MWE (AVD) R.NO- (CH)	VLSI (SD) R.NO- (CH)	IAFM (GSR) R.NO- (CH)
THURSDAY		DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	MW LAB/ EDA LAB (B/C) (AVD) / (SD)				MWE (AVD) R.NO- (CH)	MCC (NS) R.NO- (CH)	
FRIDAY		CN (SVRM) R.NO- AC LAB	MW LAB/ EDA LAB (C/A) (AVD) / (SD)				VLSI (SD) R.NO- (CH)	PROJECT SEMINAR (BB/TAK) R.NO- (CH)	

**LEGEND:**

Subject Code: Subject Name	Faculty Name
Microwave Engineering	Avinash Dasi
VLSI Design	Sudhir Dakey
Computer Networks	SVR. Manimala
Mobile Cellular Communication	Dr. N. Srinivas
Digital Image Processing Optical Fiber Communication Embedded Systems	T.. Kavitha K. Narasimha Rao B. Bhavani
Industrial Administration and Financial management	G. Srinivasa Rao
Microwave Lab	Avinash Dasi
EDA LAB	Sudhir Dakey
Project Seminar	B. Bhavani /Dr.T. Anil Kumar

**Timetable Co-ordinators.**

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**Timetable for B.E. 4/4 ECE (SEC-B) Acad. Year: 2016-17- I Semester  
W.E.F: 08-08-2016**

DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00		1.45-2.35	2.35-3.25	3.25-4.15	
MONDAY		CN (SVRM) R.NO- (CH)		MCC (NS) R.NO- (CH)		L U N C H	IAFM (GSR) R.NO- EC-106	DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB		
TUESDAY		IAFM (GSR) R.NO- AC LAB	MW LAB/ EDA LAB (A/B) (KVBLD) / (SVRM)				VLSI (SM) R.NO- (CH)	MWE (KVBLD) R.NO- (CH)		
WEDNESDAY		DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	MCC (NS) R.NO- EC-115	VLSI (SM) R.NO- EC-115			MWE (KVBLD) R.NO- EC-115	PROJECT SEMINAR (BS/ BSR) R.NO- EC-115		
THURSDAY		DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	MWE (KVBLD) R.NO- (CH)	CN (SVRM) R.NO- (CH)			MW LAB/ EDA LAB (B/C) (KVBLD) / (SVRM)			
FRIDAY		MCC (NS) R.NO- (CH)	VLSI (SM) R.NO- (CH)	IAFM (GSR) R.NO- (CH)			MW LAB/ EDA LAB (C/A) (KVBLD) / (SVRM)			

**LEGEND:**

Subject Code: Subject Name	Faculty Name
Microwave Engineering	KVBL. Deepthi
VLSI Design	Smitashree M
Computer Networks	SVR. Manimala
Mobile Cellular Communication	Dr. N. Srinivas
Digital Image Processing Optical Fiber Communication Embedded Systems	T. Kavitha K. Narasimha Rao B. Bhavani
Industrial Administration and Financial management	G. Srinivasa Rao
Microwave Lab	KVBL. Deepthi
EDA LAB	SVR. Manimala
Project Seminar	B. Sarala/ B. Srinivas

**Timetable Co-ordinators.**

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DAY ↓	TIME →	9.45-10.35	10.35-11.25	11.25-12.15	12.15-1.00	L U N C H	1.45-2.35	2.35-3.25	3.25-4.15	
MONDAY		MCC (KSRM) AC LAB	MW LAB/ EDA LAB (A/B) (EVNL) / (SBD)					VLSI (SBD) R.NO-CH	DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	
TUESDAY		VLSI (SBD) R.NO-CH		IAFM (GB) R.NO-CH				MW LAB/ EDA LAB (B/C) (EVNL) / (SBD)		
WEDNESDAY		DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	MWE (KVBLD) R.NO-CH		IAFM (GB) R.NO-CH			MW LAB/ EDA LAB (C/A) (EVNL) / (SBD)		
THURSDAY		DIP/OFC/ES (TK/ KNR/ BB) CH/ EC-115 / AC LAB	MCC (KSRM) R.NO-EC-115	CN (MD) R.NO-EC-115				IAFM (GB) R.NO-EC-115	PROJECT SEMINAR (Prof. KSRM/ NN/ TK) R.NO-EC-115	
FRIDAY		CN (MD) R.NO-EC-115		MWE (KVBLD) R.NO-EC-115			VLSI (SBD) R.NO-EC-115	MCC (KSRM) R.NO-EC-115		

**LEGEND:**

Subject Code: Subject Name	Faculty Name
Microwave Engineering	KVBL. Deepthi
VLSI Design	Shilpa B Darvesh
Computer Networks	M. Deepthi
Mobile Cellular Communication	Prof.KSR. Murthy
Digital Image Processing Optical Fiber Communication Embedded Systems	T. Kavitha K. Narasimha Rao B. Bhavani
Industrial Administration and Financial management	G. Bharathi
Microwave Lab	E.V.Naga Lakshmi
EDA LAB	Shilpa B Darvesh
Project Seminar	Prof. KSR. Murthy/ N. Namassivaya / T. Kavitha

Timetable Co-ordinators.

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