

MVSR ENGINEERING COLLEGE, ECE DEPARTMENT
Timetable for M.E. 1/2 ECE (ES&VLSID) Acad. Year: 2018-19- I Semester

Room No: EC-108
13.08.2018

W.E.F:

TIME DAY	9:30-10:30	10:30-11:30	11:30-12:30	12:30-1:15	1:15-2:15	2:15-3:15	3:15-4:15
TUE	VLSIDT (SD)	ADDVH (Dr. BS)		LUNC H	D & S - I LAB (BB/ SA)		
WED	MESD (SRK)	DSP (BSR)	AICD (Dr. GKD)		VLSIDT (SD)	VPD (SBD)	
THU	ADDVH (Dr. BS)	MESD (SRK)			VLSIDT (SD)	DSP (BSR)	
FRI	VPD (SBD)	AICD (Dr. GKD)			SEMINAR - I (Dr. VCS/ BSR)		

LEGEND:

S.N O	CODE	SUBJECT NAME	FACULTY NAME	PHONE NO.
THEORY COURSES				
1	EC 601	MICROCONTROLLERS FOR EMBDEDD SYS DESIGN	S. RAVI KUMAR	9912479084
2	EC 602	VLSI DESIGN & TECHNOLOGY	SUDHIR DAKEY	9849958558
3	EC 603	ANALOG IC DESIGN	Dr. G. KANAKA DURGA	9703804723
4	EC 606	VLSI PHYSICAL DESIGN	SHILPA B DARVESH	7799864497
5	EC 506	DIGITAL SIGNAL PROCESSORS	B. SRINIVAS	9618028110
6	EC 521	ADV. DIGITAL DESIGN WITH VERILOG HDL	Dr. B. SARALA	9502598968
PRACTICAL/LABORATORY COURSES				
7	EC607	Design and Simulation Laboratory-I	B. BHAVANI/ S. ARUNA	9052502054 9704231417

8	EC619	Seminar-I	Dr.V. CHANDRA SEKHAR / B. SRINIVAS	9966373428 9618028110
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TIME TABLE CO-ORDINATORS

CHIEF TIME TABLE CO-ORDINATORS

HOD,ECE