

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-I: Room No – CS 13

w.e.f. 27 - 07 - 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	M-III (PS)	CA (SAD)	DIS (MD)		L U N C H	BE Lab, ECE dept. Batch-A(Dr.SSR,NK) DSA Lab, Lab-7 Batch-B(SA,KP)		
Tuesday	BE (Dr.SSR)		LST (GVK)	DSA (SA)		BE Lab, ECE dept. Batch-B(Dr.SSR,NK) DSA Lab, Lab-7 Batch-C(SA,KP)		
Wednesday	CA (SAD)	BE Lab, ECE dept. Batch-C(Dr.SSR,NK) DSA Lab, Lab-7 Batch-A(SA,KP)				CRT		M-III (ME)
Thursday	DSA (SA)		BE (Dr.SSR)	M-III (ME)		CA (SAD)	LST (GVK)	DIS (MD)
Friday	LST (GVK)		DIS (MD)	CA (SAD)		M-III (PS)	BE (Dr.SSR)	DSA (SA)
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS – III	ME – A MOIZ PS – P SWAPNA
DSA	DATA STRUCTURES USING C++	SA – S AMULYA
DIS	DISCRETE STRUCTURES	MD- M DYNA
LST	LOGIC AND SWITCHING THEORY	GVK- G VIJAY KUMAR
CA	COMPUTER ARCHITECTURE	SAD – SESHAM ANAND
BE	BASIC ELECTRONICS	Dr SSR – Dr S SWAPNA RANI
DSA LAB	DATA STRUCTURES LAB	SA – S AMULYA KP – K PADMA
BE LAB	BASIC ELECTRONICS LAB	Dr SSR – Dr S SWAPNA RANI NK – N KAVITHA

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-II: Room No – CS 12

w.e.f. 27 - 07 - 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	CA (MM)	LST (TL)	DSA (KP)		L U N C H	DIS (TSK)	M-III (MJ)	BE (MSC)
Tuesday	M-III (MJ)	BE Lab, ECE dept. Batch-A(MSC,SRK) DSA Lab, Lab-7 Batch-B(KP,MLNR)				DIS (TSK)	LST (TL)	CA (MM)
Wednesday	CRT		LST (TL)	CA (MM)		BE Lab, ECE dept. Batch-B(MSC,SRK) DSA Lab, Lab-7 Batch-C(KP,MLNR)		
Thursday	LST (TL)	BE Lab, ECE dept. Batch-C(MSC,SRK) DSA Lab, Lab-2 Batch-A(KP,MLNR)				BE (MSC)	M-III (PSL)	DSA (KP)
Friday	DIS (TSK)		BE (MSC)			DSA (KP)	CA (MM)	M-III (PSL)
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS – III	MJ – M JYOTHSNA PSL- P SUMALATHA
DSA	DATA STRUCTURES USING C++	KP - K PADMA
DIS	DISCRETE STRUCTURES	TSK – T SRIKANTH
LST	LOGIC AND SWITCHING THEORY	TL - T LAKSHMI
CA	COMPUTER ARCHITECTURE	MM – M MADHURI
BE	BASIC ELECTRONICS	MSC – M SHARATH CHANDRA
DSA LAB	DATA STRUCTURES LAB	KP – K PADMA MLNR - M LAKSHMI NARASIMHA REDDY
BE LAB	BASIC ELECTRONICS LAB	MSC- M SHARATH CHANDRA SRK – S RAVI KUMAR

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-III: Room No – CS 11

w.e.f. 27 - 07 – 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	M-III (PN)	BE Lab, ECE dept. Batch-A(Dr.SSR,GSK) DSA Lab, Lab-2 Batch-B(MLNR,SA)			L U N C H	DSA (MLN)	DIS (BVR)	
Tuesday	BE (GM)	M-III (PN)	DIS (BVR)	LST (MVJS)		DSA (MLN)		CA (AA)
Wednesday	DIS (BVR)	CA (AA)	CRT			LST (MVJS)	BE (GM)	
Thursday	DSA (MLN)	CA (AA)	LST (MVJS)	BE (GM)		BE Lab, ECE dept. Batch-B(Dr.SSR,GSK) DSA Lab, Lab-2 Batch-C(MLNR,SA)		
Friday	CA (AA)	BE Lab, ECE dept. Batch-C(Dr.SSR,GSK) DSA Lab, Lab-2 Batch-A(MLNR,SA)				LST (MVJS)	M-III (PN)	
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS – III	PN – P NARENDER
DSA	DATA STRUCTURES USING C++	MLNR - M LAKSHMI NARASIMHA REDDY
DIS	DISCRETE STRUCTURES	BVR - B VENKATA RAMANA
LST	LOGIC AND SWITCHING THEORY	MVJS - MVR JYOTHISREE
CA	COMPUTER ARCHITECTURE	AA – ABDUL AZEEM
BE	BASIC ELECTRONICS	GM- G MAHENDER
DSA LAB	DATA STRUCTURES LAB	MLNR - M LAKSHMI NARASIMHA REDDY SA - S AMULYA
BE LAB	BASIC ELECTRONICS LAB	Dr SSR – Dr S SWAPNA RANI GSK- G SHRAVAN KUMAR

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-I: Room No – CS 203

w.e.f. 20 - 07 – 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	MEA (MSRR)	ALC (SV)		DBMS (BS)	C H L U N C H	SE (SP)	OS (KMK)	DAA (KVSL)
Tuesday	OS (KMK)	SE (SP)	DBMS (BS)			MINI PROJECTS (Dr.CHS, BVR, SV, VN)		
Wednesday	SE (SP)	DBMS Lab, Lab-3 Batch-A (BS,KM) OS Lab, Lab-4 Batch-B (KMK,TSA)				CRT		MEA (MSRR)
Thursday	DAA (KVSL)		OS (KMK)	MEA (MSRR)		ALC (SV)	DBMS (BS)	SE (SP)
Friday	ALC (SV)	OS (KMK)	DAA (KVSL)	MEA (MSRR)		DBMS Lab, Lab-3 Batch-B (BS,KM) OS Lab, Lab-4 Batch-A (KMK,TSA)		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	BS – B SARITHA
OS	OPERATING SYSTEMS	KMK- K MURALI KRISHNA
ALC	AUTOMATA LANGUAGES AND COMPUTATION	SV - V SATHISH
SE	SOFTWARE ENGINEERING	SP - P SHALINI
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	MSRR- M SRIRANGARAJAN
DAA	DESIGN AND ANALYSIS OF ALGORITHMS	KVSL – K V SREE LAKSHMI
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	BS- B SARITHA KM – K MADHURI
OS LAB	OPERATING SYSTEMS LAB	KMK - K MURALI KRISHNA TSA – T SUJANAVAN
	MINI PROJECT	Dr CHS - Dr CH SAMSON BVR – B VENKATARAMANA SV - V SATHISH VN – VIKRAM NARAYANDAS

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-II: Room No – CS 204

w.e.f. 27 - 07 – 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DAA (BRK)		ALC (MA)		L U N C H	SE (PT)	DBMS (KM)	MEA (MSRR)
Tuesday	SE (PT)	ALC (MA)		MEA (MSRR)		DBMS (KM)	OS (TSA)	
Wednesday	CRT		SE (PT)	DAA (BRK)		DBMS Lab, Lab-3 Batch-A (KM,GSS) OS Lab, Lab-4 Batch-B (TSA,DH)		
Thursday	DBMS (KM)	DBMS Lab, Lab-3 Batch-B (KM,GSS) OS Lab, Lab-4 Batch-A (TSA,DH)				OS (TSA)	DAA (BRK)	MEA (MSRR)
Friday	OS (TSA)	MEA (MSRR)	DBMS (KM)	SE (PT)		MINI PROJECTS (SAD, BVR, SV, VN)		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	KM - K MADHURI
OS	OPERATING SYSTEMS	TSA – T SUJANAVAN
ALC	AUTOMATA LANGUAGE AND COMPUTATION	MA – M ANUPAMA
SE	SOFTWARE ENGINEERING	PT – P TEJASWI
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	RR – RANGARAJAN
DAA	DESIGN AND ANALYSIS OF ALGORITHMS	BRK – B RANJITH KUMAR
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	KM – K MADHURI GSS – G SRISAILAM
OS LAB	OPERATING SYSTEMS LAB	TSA – T SUJANAVAN DH- D HARITHA
	MINI PROJECT	SAD - SESHAM ANAND BVR – B VENKATARAMANA SV – V SATHISH VN- VIKRAM NARAYANDAS

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-III: Room No – CS 09

w.e.f. 27 - 07 - 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DBMS (GSS)	DAA (BJ)	MEA (MSRR)	SE (GM)	L U N C H	DBMS Lab, Lab-3 Batch-A (GSS,BS) OS Lab, Lab-4 Batch-B (DH,TSA)		
Tuesday	SE (GM)	DAA (BJ)	OS (DH)			DBMS (GSS)	ALC (MA)	MEA (MSRR)
Wednesday	ALC (MA)		CRT			MEA (MSRR)	SE (GM)	DAA (BJ)
Thursday	DAA (BJ)	MEA (MSRR)	ALC (MA)	SE (GM)		OS (DH)	DBMS (GSS)	
Friday	OS (DH)	DBMS Lab, Lab-3 Batch-B (GSS,BS) OS Lab, Lab-4 Batch-A (DH,TSA)				MINI PROJECT (Dr.SB, GM, BJ)		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	GSS – G SRISAILAM
OS	OPERATING SYSTEMS	DH – D HARITHA
ALC	AUTOMATA LANGUAGE AND COMPUTATION	MA – M ANUPAMA
SE	SOFTWARE ENGINEERING	GM – GOUTAM MURTHY
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	RR – RANGARAJAN
DAA	DESIGN AND ANALYSIS OF ALGORITHMS	BJ – B JANAI AH
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	GSS – G SRISAILAM BS – B SARITHA
OS LAB	OPERATING SYSTEMS LAB	DH – D HARITHA TSA – T SUJANAVAN
	MINI PROJECT	Dr SB – Dr B SANDHYA GM – GOUTAM MURTHY BJ - B JANAI AH

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-I: Room No – CS 205

w.e.f. 27 - 07 - 2015

	1	2	3	4		5	6	7	
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm		01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DS (PS)	DS Lab, Lab-3 Batch-A (PS, KMK) EBS Lab, Lab-6 Batch-B (PUG, GVK, MVJS)			L U C H	TUTORIAL / LIBRARY			
Tuesday	PAES (PUG)	IS (Dr CHS)	AI (Dr SB)			DS Lab, Lab-3 Batch-B (PS, KMK) EBS Lab, Lab-6 Batch-A (PUG, GVK, MVJS)			
Wednesday	IS (Dr CHS)	PAES (PUG)		E-1		TUTORIAL / LIBRARY			
Thursday	E-1	DS (PS)	IS (Dr CHS)	AI (Dr SB)		PROJECT SEMINARS (Dr.MH, Dr.SB, Dr.CHS, BVR)			
Friday	AI (Dr SB)	PAES (PUG)	E-1			IS (Dr CHS)	DS (PS)		
Saturday									

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	PS - - P SUBHASHINI
AI	ARTIFICIAL INTELLIGENCE	Dr SB- Dr B SANDHYA
IS	INFORMATION SECURITY	Dr CHS- Dr CH SAMSON
EBS	PRINCIPLES OF EMBEDDED SYSTEMS	PUG - P USHA GAYATRI
E-I (SPM)	ELECTIVE – I	VN- VIKRAM NARAYANDAS SP – P SHALINI
DS LAB	DISTRIBUTED SYSTEMS LAB	PS – P SUBHASHINI KMK – K MURALI KRISHNA
EBS LAB	EMBEDDED SYSTEMS LAB	PUG - P USHA GAYATRI GVK – G VIJAY KUMAR MVJS – M V JTO THI SREE
	PROJECT SEMINARS	Dr. MH – Dr M HANUMANDLU Dr.SB – Dr.B SANDHYA Dr CHS- Dr CH SAMSON BVR – B VENKATA RAMANA

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-II: Room No – IT 201

w.e.f. 27 - 07 - 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	IS (PPP)	PAES (DS)		DS (VS)	L U N C H	TUTORIAL / LIBRARY		
Tuesday	AI (Dr AK)	DS Lab, Lab-4 Batch-A (AA,PS) EBS Lab, Lab-6 Batch-B (PUG,TL,PT)				PAES (DS)	DS (VS)	IS (PPP)
Wednesday	PAES (DS)	DS (VS)		E-1		TUTORIAL / LIBRARY		
Thursday	E-1	IS (PPP)	AI (Dr AK)			DS Lab, Lab-4 Batch-B (AA,PS) EBS Lab, Lab-6 Batch-A (PUG,TL,PT)		
Friday	IS (PPP)	AI (Dr AK)	E-1			PROJECT SEMINARS (Dr.MH, Dr.AK, MA)		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	VS - V SRIDHAR
AI	ARTIFICIAL INTELLIGENCE	Dr AK - Dr AKHIL KHARE
IS	INFORMATION SECURITY	PPP – P PHANI PRASAD
EBS	PRINCIPLES OF EMBEDDED SYSTEMS	DS- D SIRISHA
E-I	ELECTIVE – I	Dr CHS- Dr CH SAMSON SP – P SHALINI
DS LAB	DISTRIBUTED SYSTEMS LAB	AA- ABDUL AZEEM PS – P SUBHASHINI
EBS LAB	EMBEDDED SYSTEMS LAB	PUG – P USHA GAYATRI TL – T LAKSHMI PT – P TEJASWI
	PROJECT SEMINARS	Dr. MH – Dr M HANUMANDLU Dr AK - Dr AKHIL KHARE MA – M ANUPAMA

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2015-16 from 20 - 07 - 2015 to 07 - 11 - 2015)

Section-III: Room No – S & H 101

w.e.f. 27 - 07 - 2015

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DS (VS)	AI (Dr AK)	IS (VN)		L U N C H	DS Lab, Lab-2 Batch-A (VS,AA) EBS Lab, Lab-6 Batch-B (DS,GVK,MM)		
Tuesday	DS (VS)	PAES (DS)		AI (Dr AK)		PROJECT SEMINARS (Dr.MH, Dr.AK, GM, BJ)		
Wednesday	IS (VN)	AI (Dr AK)		E-1		DS Lab, Lab-2 Batch-B (VS,AA) EBS Lab, Lab-6 Batch-A (DS,GVK,MM)		
Thursday	E-1	PAES (DS)	DS (VS)	IS (VN)		TUTORIAL / LIBRARY		
Friday	PAES (DS)	DS (VS)	E-1			TUTORIAL / LIBRARY		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	VS – V SRIDHAR
AI	ARTIFICIAL INTELLIGENCE	Dr AK - Dr AKHIL KHARE
IS	INFORMATION SECURITY	VN – VIKRAM NARAYANDAS
EBS	PRINCIPLES OF EMBEDDED SYSTEMS	DS – D SIRISHA
E-I	ELECTIVE – I	MVJS - MVR JYOTHISREE SP – P SHALINI
DS LAB	DISTRIBUTED SYSTEMS LAB	VS – V SRIDHAR AA – ABDUL AZEEM
EBS LAB	EMBEDDED SYSTEMS LAB	DS – D SIRISHA GVK – G VIJAY KUMAR MM – M MADHURI
	PROJECT SEMINARS	Dr. MH – Dr M HANUMANDLU Dr AK – Dr AKHIL KHARE GM – GAUTAM MURTHY BJ – B JANAI AH

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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