

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section-I: Room No – CS 13

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DSC++ (MLNR)	LST (GVK)	BE (MSC)	M-III (Dr AN)	L U N D S C H	DSC++ Lab (Batch-A), Lab-7(MLN,AS) BE Lab(Batch-B) (MSC,MR)		
Tuesday	CA (SAD)	DSC++ (MLNR)	M-III (Dr AN)	DIS (BJ)		DSC++ Lab (Batch-B), Lab-7(MLNR,AS) BE Lab(Batch-C) (MSC,MR)		
Wednesday	DIS (BJ)	DSC++ Lab (Batch-C), Lab-7(MLN,AS) BE Lab(Batch-A) (MSC,MR)				CA (SAD)	CRT	
Thursday	LST (GVK)		DSC++ (MLNR)	CA (SAD)		M-III (Dr AN)	BE (MSC)	DIS (BJ)
Friday	M-III (Dr AN)	DIS (BJ)	CA (SAD)	LST (GVK)		DSC++ (MLNR)	BE (MSC)	
Saturday	TUTORIAL / LIBRARY							

LEGEND

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS-III	Dr AN – Dr A NEELIMA
DSC++	DATA STRUCTURES USING C++	MLNR – M L N REDDY
DIS	DISCRETE STRUCTURES	BJ – B JANAI AH
LST	LOGIC AND SWITCHING THEORY	GVK – G VIJAY KUMAR
CA	COMPUTER ARCHITECTURE	SAD – SESHAM ANAND
BE	BASIC ELECTRONICS	MSC – M SHARATH CHANDRA
DSC++ LAB	DATA STRUCTURES LAB	MLNR – M L N REDDY AS – A SARITHA
BE LAB	BASIC ELECTRONICS LAB	MSC – M SHARATH CHANDRA MR – M RAMESH

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section-II : Room No – CS 12

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	BE (MD)	DSC++ (AS)	CA (AA)	LST (GVK)	L U N C H	DIS (KP)	M-III (USR)	
Tuesday	M-III (USR)	DSC++ Lab (Batch-A), Lab-7(AS,VS) BE Lab(Batch-B) (MD,KNR)				LST (GVK)	BE (MD)	
Wednesday	CRT		LST (GVK)	DIS (KP)		DSC++ Lab (Batch-B), Lab-7(AS,VS) BE Lab(Batch-C) (MD,KNR)		
Thursday	DIS (KP)	DSC++ Lab (Batch-C), Lab-7(AS,VS) BE Lab(Batch-A) (MD,KNR)				DSC++ (AS)	CA (AA)	BE (MD)
Friday	LST (GVK)	CA (AA)		M-III (USR)		DIS (KP)	DSC++ (AS)	
Saturday	TUTORIAL / LIBRARY							

LEGEND

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS-III	USR- U SHANMUKHA RAO
DSC++	DATA STRUCTURES USING C++	AS – A SARITHA
DIS	DISCRETE STRUCTURES	KP – K PADMA
LST	LOGIC AND SWITCHING THEORY	GVK – G VIJAY KUMAR
CA	COMPUTER ARCHITECTURE	AA – ABDUL AZEEM
BE	BASIC ELECTRONICS	MD – M DEEPTHI
DSC++ LAB	DATA STRUCTURES LAB	AS – A SARITHA VS – V SRIDHAR
BE LAB	BASIC ELECTRONICS LAB	MD – M DEEPTHI KNR- K NARASIMHA RAO

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section – III : Room No – CS 11

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7	
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm		01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DSC++ (VS)	DSC++ Lab (Batch-A), Lab-7(VS,MLN) BE Lab(Batch-B) (VRR,SD,GVCR)			L U N C H	LST (TL)	BE (VRR)	M-III (MJ)	
Tuesday	BE (VRR)	LST (TL)		M-III (MJ)		DIS (TSK)	CA (MM)	DSC++ (VS)	
Wednesday	M-III (MJ)	DSC++ (VS)	CRT			LST (TL)	DIS (TSK)	CA (MM)	
Thursday	DIS (TSK)		CA (MM)	M-III (MJ)		DSC++ Lab (Batch-B), Lab-7(VS,MLN) BE Lab(Batch-C) (VRR,SD,GVCR)			
Friday	CA (MM)	DSC++ Lab (Batch-C), Lab-7(VS,MLN) BE Lab(Batch-A) (VRR,SD,GVCR)				DSC++ (VS)	BE (VRR)		
Saturday	TUTORIAL / LIBRARY								

LEGEND

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS-III	MJ – M JYOTSHNA
DSC++	DATA STRUCTURES USING C++	VS – V SRIDHAR
DIS	DISCRETE STRUCTURES	TSK – T SRIKANTH
LST	LOGIC AND SWITCHING THEORY	TL – T LAKSHMI
CA	COMPUTER ARCHITECTURE	MM – M MADHURI
BE	BASIC ELECTRONICS	VRR – V RAJITHA RANI
DSC++ LAB	DATA STRUCTURES LAB	VS – V SRIDHAR MLNR – M L N REDDY
BE LAB	BASIC ELECTRONICS LAB	VRR – V RAJITHA RANI SD – SHILPA B DHARVESH GVCR – G V CHALAPATI RAO

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section – I : Room No – CS 09

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	ALC (MA)		OS (KMK)	SE (NP)	L U N C H	DC (BS)	MEA (MSRR)	DBMS (BVR)
Tuesday	DBMS (BVR)		MEA (MSRR)	OS (KMK)		DBMS Lab(Batch A) Lab-3(BVR,GSS) OS Lab(Batch-B) Lab-4(KMK,DH)		
Wednesday	MEA (MSRR)	DBMS Lab(Batch B) Lab-3(BVR,GSS) OS Lab(Batch-A) Lab-4(KMK,DH)				SE (NP)	CRT	
Thursday	DC (BS)	ALC (MA)	DBMS (BVR)			OS (KMK)	SE (NP)	MEA (MSRR)
Friday	OS (KMK)	DC (BS)		SE (NP)		MINI PROJECT		
Saturday	TUTORIAL / LIBRARY							

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	BVR – B VENKATARAMANA
OS	OPERATING SYSTEMS	KMK- K MURALI KRISHNA
ALC	AUTOMATA, LANGUAGES & COMPUTATION	MA – M ANUPAMA
SE	SOFTWARE ENGINEERING	NP – N PRIYANKA
MEA	MANAGERIAL ECONOMICS & ACCOUNTANCY	MSRR – M S RANGARAJAN
DC	DATA COMMUNICATIONS	BS – B SARITHA
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	BVR – B VENKATARAMANA GSS – G SRISAILAM
OS LAB	OPERATING SYSTEMS LAB	KMK- K MURALI KRISHNA DH – D HARITHA
MPJ	MINI PROJECT	MA – M ANUPAMA VN – VIKRAM NARAYANDAS

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section – II : Room No – CS 203

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	MEA (MSRR)	DBMS (Dr SB)		ALC (Dr HJ)	L U N C H	OS (DH)		SE (PPP)
Tuesday	OS (DH)	SE (PPP)	ALC (Dr HJ)			MEA (MSRR)	DBMS (Dr SB)	DC (KM)
Wednesday	CRT		MEA (MSRR)	SE (PPP)		DBMS Lab(Batch A) Lab-3(DrSB,BVR) OS Lab(Batch-B) Lab-4(DH,TSA)		
Thursday	ALC (Dr HJ)	MEA (MSRR)	DBMS (Dr SB)	DC (KM)		DBMS Lab(Batch B) Lab-3(DrSB,BVR) OS Lab(Batch-A) Lab-4(DH,TSA)		
Friday	SE (PPP)	OS (DH)	DC (KM)			MINI PROJECT		
Saturday	TUTORIAL / LIBRARY							

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	Dr SB – Dr B SANDHYA
OS	OPERATING SYSTEMS	DH – D HARITHA
ALC	AUTOMATA, LANGUAGES & COMPUTATION	Dr HJ – Dr H JAYASREE
SE	SOFTWARE ENGINEERING	PPP – P PHANI PRASAD
MEA	MANAGERIAL ECONOMICS & ACCOUNTANCY	MSRR – M S RANGARAJAN
DC	DATA COMMUNICATIONS	KM – K MADHURI
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	Dr SB – Dr B SANDHYA BVR – B VENKATARAMANA
OS LAB	OPERATING SYSTEMS LAB	DH – D HARITHA TSA – T SUJANAVAN
MPJ	MINI PROJECT	KM – K MADHURI TL T LAKSHMI

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section – III : Room No – CS 204

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	SE (MVRJ)	DC (DS)	DBMS (GSS)	OS (TSA)	L U N C H	DBMS Lab(Batch A)Lab-(GSS,SV) OS Lab(Batch-B) Lab-4(TSA,KMK)		
Tuesday	DBMS (GSS)	MEA (MSRR)	ALC (SV)	OS (TSA)		SE (MVRJ)	DC (DS)	
Wednesday	ALC (SV)	SE (MVRJ)	CRT			MEA (MSRR)	DBMS (GSS)	
Thursday	OS (TSA)		MEA (MSRR)	DC (DS)		SE (MVRJ)	ALC (SV)	
Friday	MEA (MSRR)	DBMS Lab(Batch B) Lab-3(GSS,SV) OS Lab(Batch-A) Lab-4(TSA,KMK)				MINI PROJECT		
Saturday	TUTORIAL / LIBRARY							

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	GSS – G SRISAILAM
OS	OPERATING SYSTEMS	TSA – T SUJANAVAN
ALC	AUTOMATA, LANGUAGES & COMPUTATION	SV – V SATHISH
SE	SOFTWARE ENGINEERING	MVRJ – MVR JYOTHISREE
MEA	MANAGERIAL ECONOMICS & ACCOUNTANCY	MSRR – M S RANGARAJAN
DC	DATA COMMUNICATIONS	DS – D SISRISHA
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	GSS – G SRISAILAM SV – V SATHISH
OS LAB	OPERATING SYSTEMS LAB	TSA – T SUJANAVAN KMK – K MURALIKRISHNA
MPJ	MINI PROJECT	BJ – B JANAIHAH SV – V SATHISH

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section – I : Room No – CS 205

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7	
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm		01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	E-1 MC (MM)	IS (VN)	DS (PS)	EBS (DS)	L U N C H	DS LAB, Batch-A, LAB-II (PS,GM) EBS LAB, Batch-B, LAB-VII (DS,MM)			
Tuesday	IS (VN)	E-1 MC (MM)	AI (BS)			PROJECT SEMINAR			
Wednesday	TUTORIAL / LIBRARY	DS LAB, Batch-B, LAB-II (PS,GM) EBS LAB, Batch-A, LAB-VII (DS,MM)				AI (BS)	DS (PS)	TUTORIAL / LIBRARY	
Thursday	DS (PS)		IS (VN)	AI (BS)		E-1 MC (MM)	EBS (DS)	TUTORIAL / LIBRARY	
Friday	EBS (DS)		E-1 MC (MM)	IS (VN)		TUTORIAL / LIBRARY			
Saturday	TUTORIAL / LIBRARY								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	PS - P SUBHASHINI
AI	ARTIFICIAL INTELLIGENCE	BS - B SARITHA
IS	INFORMATION SECURITY	VN - VIKRAM NARAYANDAS
EBS	PRINCIPLES & APPS. OF EMBEDDED SYSTEMS	DS – D SIRISHA
E-I MC	ELECTIVE – I, MOBILE COMPUTING	MM – M MADHURI
DS LAB	DISTRIBUTED SYSTEMS LAB	PS - P SUBHASHINI GM – G MADHU
EBS LAB	EMBEDDED SYSTEMS LAB	DS – D SIRISHA MM – M MADHURI
	PROJECT SEMINAR	PS – P SUBHASHINI(PROJECT COORDINATOR) Dr MH – Dr M HANUMANDLU MA – M ANUPAMA BS – B SARITHA

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section – II : Room No – CS 201

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DS (GM)		EBS (PUG)	IS (VN)	L U N C H	AI (Dr.SB)	E-1 MC (MVRJ)	TUTORIAL / LIBRARY
Tuesday	TUTORIAL / LIBRARY	DS LAB, Batch-A, LAB-II (GM,NS) EBS LAB, Batch-B, LAB-VII PUG, GVK				TUTORIAL / LIBRARY		
Wednesday	IS (VN)	AI (Dr.SB)		E-1 MC (MVRJ)		DS LAB, Batch-B, LAB-II (GM,NS) EBS LAB, Batch-A, LAB-VII PUG, GVK		
Thursday	AI (Dr.SB)	EBS (PUG)		E-1 MC (MVRJ)		IS (VN)	DS (GM)	TUTORIAL / LIBRARY
Friday	EBS (PUG)	IS (VN)	E-1 MC (MVRJ)	DS (GM)		PROJECT SEMINAR		
Saturday	TUTORIAL / LIBRARY							

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	GM – G MADHU
AI	ARTIFICIAL INTELLIGENCE	Dr.SB – Dr. B SANDHYA
IS	INFORMATION SECURITY	VN – VIKRAM NARAYANDAS
EBS	PRINCIPLES & APPS. OF EMBEDDED SYSTEMS	PUG – P USHA GAYATRI
E-I MC	ELECTIVE – I, MOBILE COMPUTING	MVRJ – M V R JYOTHI SRI
DS LAB	DISTRIBUTED SYSTEMS LAB	GM – G MADHU NS – N SABITHA
EBS LAB	EMBEDDED SYSTEMS LAB	PUG – P USHA GAYATRI GVK – G VIJAY KUMAR
	PROJECT SEMINAR	KMK – K MURALI KRISHNA (PROJECT COORDINATOR) Dr HJ – Dr H JAYASREE BS – B SARITHA AA – ABDUL AZEEM

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2016-17 from 11 – 07 – 2016 to 23-11-2016)

Section – III : Room – Seminar Hall

w. e. f. - 18 – 07 – 2016

	1	2	3	4		5	6	7		
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm		01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm	
Monday	TUTORIAL / LIBRARY	EBS (TL)	AI (Dr.AK)		L U N C H	DS (NS)	IS (NP)	TUTORIAL / LIBRARY		
Tuesday	DS (NS)	AI (Dr.AK)	IS (NP)	E-1 MC (KM)		TUTORIAL / LIBRARY				
Wednesday	EBS (TL)		IS (NP)	DS (NS)		AI (Dr.AK)	E-1 MC (KM)			
Thursday	E-1 MC (KM)	DS (NS)	IS (NP)	EBS (TL)		DS LAB, Batch-A, LAB-II (NS,PS) EBS LAB, Batch-B, LAB-VII (TL,PUG)				
Friday	TUTORIAL / LIBRARY	DS LAB, Batch-B, LAB-II (NS,PS) EBS LAB, Batch-A, LAB-VII(TL,PUG)				PROJECT SEMINAR				
Saturday	TUTORIAL / LIBRARY									

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	NS – N SABITHA
AI	ARTIFICIAL INTELLIGENCE	Dr.AK – Dr. AKHIL KHARE
IS	INFORMATION SECURITY	NP – N PRIYANKA
EBS	PRINCIPLES & APPS. OF EMBEDDED SYSTEMS	TL – T LAKSHMI
E-I MC	ELECTIVE – I, MOBILE COMPUTING	KM – K MADHURI
DS LAB	DISTRIBUTED SYSTEMS LAB	NS – N SABITHA PS – P SUBHASHINI
EBS LAB	EMBEDDED SYSTEMS LAB	TL – T LAKSHMI PUG – P USHA GAYATRI
	PROJECT SEMINAR	TSA – T SUJANAVAN(PROJECT COORDINATOR) Dr MH – Dr M HANUMANDLU Dr AK – Dr AKHIL KHARE SAD – SESHAM ANAND

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
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