

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-I: Room No - CS 09

w. e. f. - 31 - 07 - 2017

	1	2		3	4		5	6
	09:30 am To 10.30 am	10:30 am To 11:30 am	B R E A K	11:40 am To 12:40 pm	12:40 pm To 01:40 pm	L U N C H	02:15 pm To 03:15 pm	03:15 pm To 04:15 pm
Monday	DSA(VS)	LST(GVK)		DIS(BJ)	ES(GP)			
Tuesday	BE(LK)	DIS(BJ)		DSA LAB (B1) - VS,PPP/ BE LAB(B2)/EE LAB(B3)			LST(GVK)	M-III (KRB)
Wednesday	DSA LAB (B2) - VS,PPP/ BE LAB(B3)/EE LAB(B1)			M-III (KRB)	BE(LK)		DSA(VS)	DIS(BJ)
Thursday	DSA LAB (B1,B2,B3) (VS,SA,KP,PPP)			DSA(VS)	LST(GVK)		M-III (KRB)	BE(LK)
Friday	DIS(BJ)	LST(GVK)		DSA(VS)	ES(GP)		DSA LAB (B3) - VS,PPP/ BE LAB(B1)/EE LAB(B2)	
Saturday	M-III (KRB)	BE(LK)		ES(GP)				

LAB LOCATIONS:	DSA Lab: LAB-2, 3 Ground Floor		BE Lab : ECE Block
	EE Lab : EEE Block Ground Floor Room No. M-11		
Batch-1(B1) : 001 to 024		Batch-2(B2) : 025 to 048	Batch-3(b3) : 049 to 060, 301 to 312

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	ENGG. MATHEMATICS-III	K RAMESH BABU
DSA	DATA STRUCTURES	V SRIDHAR
DIS	DISCRETE MATHEMATICS	B JANAI AH
LST	LOGIC AND SWITCHING THEORY	G VIJAY KUMAR
ES	ENVIRONMENTAL SCIENCES	G PADMAJA
BE	BASIC ELECTRONICS	L KARTHIK
DSA LAB	DATA STRUCTURES LAB	V SRIDHAR, P PHANI PRASAD, K PADMA, A.SARITHA
BE LAB	BASIC ELECTRONICS LAB	L KARTHIK
EE LAB	ELECTRICAL ENGG. LAB	P B GURU PRASANNA, N BHARATH MOHAN

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester - 1 (Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-II: Room No - CS 11

w. e. f. - 31 - 07 - 2017

	1	2		3	4		5	6	
	09:30 am To 10.30 am	10:30 am To 11:30 am	B R E A K	11:40 am To 12:40 pm	12:40 pm To 01:40 pm	L U N C H	02:15 pm To 03:15 pm	03:15 pm To 04:15 pm	
Monday	DSA LAB (B1) - AS,KVSL/ BE LAB(B2)/EE LAB(B3)			BE(MSC)	ES(ASK)		DSA(AS)	M-III(ME)	
Tuesday	DSA LAB (B1,B2,B3) (AS,KP,VS,KVSL)			ES(ASK)	DSA(AS)				
Wednesday	LST (KVSL)	DIS(BJ)		DSA(AS)			DSA LAB (B2) - AS,KVSL/ BE LAB(B3)/EE LAB(B1)		
Thursday	DIS(BJ)			LST (KVSL)	BE(MSC)		M-III(ME)		
Friday	BE(MSC)	LST (KVSL)		DSA LAB (B3) - AS,KVSL/ BE LAB(B1)/EE LAB(B2)			DIS(BJ)	ES(ASK)	
Saturday	BE(MSC)	M-III(ME)		LST (KVSL)					

LAB LOCATIONS:	DSA Lab: LAB-2, 3 Ground Floor	BE Lab : ECE Block
	EE Lab : EEE Block Ground Floor Room No. M-11	
Batch-1(B1) : 061 to 084	Batch-2(B2) : 085 to 108	Batch-3(B3) : 109 to 120, 313 to 324

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	ENGG. MATHEMATICS-III	MOIZ EHTESHAMUDDIN
DSA	DATA STRUCTURES	A SARITHA
DIS	DISCRETE MATHEMATICS	B JANAI AH
LST	LOGIC AND SWITCHING THEORY	K V S LAKSHMI
ES	ENVIRONMENTAL SCIENCES	A SANJAY KUMAR
BE	BASIC ELECTRONICS	M SHARATH CHANDRA
DSA LAB	DATA STRUCTURES LAB	A SARITHA, K V S LAKSHMI, V SRIDHAR, K PADMA
BE LAB	BASIC ELECTRONICS LAB	M SHARATH CHANDRA
EE LAB	ELECTRICAL ENGG. LAB	V RAGHU KRISHNA, N RAVI

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-III: Room No - CS 12

w. e. f. - 31 - 07 - 2017

	1	2		3	4		5	6
	09:30 am To 10.30 am	10:30 am To 11:30 am	B R E A K	11:40 am To 12:40 pm	12:40 pm To 01:40 pm	L U N C H	02:15 pm To 03:15 pm	03:15 pm To 04:15 pm
Monday	M-III (PSL)	ES(ASK)		DSA(KP)	LST(GVK)		DSA LAB (B1) - KP,TSK/ BE LAB(B2)/EE LAB(B3)	
Tuesday	DIS(TSK)	LST(GVK)		M-III (PSL)	BE(GSK)		DSA LAB (B1,B2,B3) (KP,VS,AS,TSK)	
Wednesday	DSA(KP)			DSA LAB (B2) - KP,TSK/ BE LAB(B3)/EE LAB(B1)			LST(GVK)	DIS(TSK)
Thursday	ES(ASK)	BE(GSK)		DIS(TSK)			LST(GVK)	
Friday	DSA LAB (B3) - KP,TSK/ BE LAB(B1)/EE LAB(B2)			ES(ASK)	BE(GSK)		DSA(KP)	M-III (PSL)
Saturday	BE(GSK)	M-III (PSL)						

LAB LOCATIONS:	DSA Lab: LAB-2, 3 Ground Floor	BE Lab : ECE Block
	EE Lab : EEE Block Ground Floor Room No. M-11	
Batch-1(B1) : 121 to 144 Batch-2(B2) : 145 to 168 Batch-3(B3) : 169 to 180, 325 to 336		

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	ENGG. MATHEMATICS-III	P SUMA LATHA
DSA	DATA STRUCTURES	K PADMA
DIS	DISCRETE MATHEMATICS	T SRIKANTH
LST	LOGIC AND SWITCHING THEORY	G VIJAY KUMAR
ES	ENVIRONMENTAL SCIENCES	A SANJAY KUMAR
BE	BASIC ELECTRONICS	G SRAVAN KUMAR
DSA LAB	DATA STRUCTURES LAB	K PADMA, T SRIKANTH, A SARITHA, SRIDHAR
BE LAB	BASIC ELECTRONICS LAB	G SRAVAN KUMAR
EE LAB	ELECTRICAL ENGG. LAB	N RAVI, S SHYAM MOHAN

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-I: Room No - CS 203

w. e. f. - 31 - 07 - 2017

	1	2	3	4		5	6	7	
	09:45 am To 10.35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm		01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	OS (TSA)	SE (MVRJ)	DC (KMK)	MEA (SRR)	LUNCH	DBMS LAB BATCH-A (BVR,SV) OS LAB BATCH-B (TSA,DH)			
Tuesday	OS (TSA)		ALC (MA)			DBMS (BVR)	DC (KMK)		
Wednesday	DC (KMK)	DBMS (BVR)		SE (MVRJ)		OS (TSA)	MEA (SRR)		
Thursday	DBMS (BVR)	ALC (MA)		DC (KMK)		SE (MVRJ)	MEA (SRR)		
Friday	SE (MVRJ)	DBMS LAB BATCH-B (BVR,SV) OS LAB BATCH-A (TSA,DH)				MINI PROJECT (SA, MVRJ,VN)			
Saturday									

LAB LOCATIONS: DBMS LAB: LAB-3 Ground Floor OS LAB : LAB-4 Ground Floor

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
OS	OPERATING SYSTEMS	T SUJANAVAN
ALC	AUTOMATA, LANGUAGES AND COMPUTATION	M ANUPAMA
DBMS	DATABASE MANAGEMENT SYSTEMS	B VENKATA RAMANA
DC	DATA COMMUNICATION	K MURALI KRISHNA
SE	SOFTWARE ENGINEERING	M V R JYOTISREE
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	SRI RANGARAJU
DBMS LAB	DATABASE MANAGEMENT SYSTEM LAB	B VENKATA RAMANA, V SATHISH
OS LAB	OPERATING SYSTEM LAB	T SUJANAVAN, D HARITHA
MINI PROJECT	MINI PROJECT	N VIKRAM, S AMULYA, M V R JYOTISREE

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-II: Room No - CS 204

w. e. f. - 31 - 07 - 2017

	1	2	3	4		5	6	7
	09:45 am To 10.35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	MEA (SRR)	DBMS LAB BATCH-A (GSS,BVR) OS LAB BATCH-B (DH,TSA)			L U N C H	ALC (HJ)	DBMS (GSS)	DC (KM)
Tuesday	MEA (SRR)	OS (DH)		DC (KM)		DBMS (GSS)	SE (MLN)	
Wednesday	OS (DH)	ALC (HJ)	DC (KM)	SE (MLN)		MINI PROJECT (JPK,MVRJ,VN)		
Thursday	SE (MLN)	DBMS (GSS)	MEA (SRR)	ALC (HJ)		DBMS LAB BATCH-B (GSS,BVR) OS LAB BATCH-A (DH,TSA)		
Friday	DC (KM)	DBMS (GSS)	ALC (HJ)	MEA (SRR)		SE (MLN)	OS (DH)	
Saturday								

LAB LOCATIONS: DBMS LAB: LAB-3 Ground Floor OS LAB : LAB-4 Ground Floor

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
OS	OPERATING SYSTEMS	D HARITHA
ALC	AUTOMATA, LANGUAGES AND COMPUTATION	Dr. H JAYASREE
DBMS	DATABASE MANAGEMENT SYSTEMS	G SRISAILAM
DC	DATA COMMUNICATION	K MADHURI
SE	SOFTWARE ENGINEERING	M L N REDDY
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	SRI RANGARAJU
DBMS LAB	DATABASE MANAGEMENT SYSTEM LAB	G SRISAILAM, B VENKATA RAMANA
OS LAB	OPERATING SYSTEM LAB	D HARITHA, T SUJANAVAN
MINI PROJECT	MINI PROJECT	J PRASANNA KUMAR, M V R JYOTISREE, N VIKRAM

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-III: Room No - CS 13

w. e. f. - 31 - 07 - 2017

	1	2	3	4		5	6	7	
	09:45 am To 10.35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm		01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	ALC (MA)		DC (KM)	DBMS (SB)	L U N C H	OS (AK)		MEA (SRR)	
Tuesday	SE (JPK)	OS (AK)	DBMS (SB)	MEA (SRR)		MINI PROJECT (H),KM,MVRJ)			
Wednesday	SE (JPK)	DBMS LAB BATCH-A (SB,SV) OS LAB BATCH-B (KMK,AK)				ALC (MA)			
Thursday	SE (JPK)	MEA (SRR)	DBMS (SB)	OS (AK)		DC (KM)			
Friday	SE (JPK)	DBMS (SB)	MEA (SRR)	DC (KM)		DBMS LAB BATCH-B (SB,SV) OS LAB BATCH-A (KMK,AK)			
Saturday									

LAB LOCATIONS: DBMS LAB: LAB-3 Ground Floor OS LAB : LAB-4 Ground Floor

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
OS	OPERATING SYSTEMS	Dr. AKHIL KHARE
ALC	AUTOMATA, LANGUAGES AND COMPUTATION	M ANUPAMA
DBMS	DATABASE MANAGEMENT SYSTEMS	Dr. B SANDHYA
DC	DATA COMMUNICATION	K MADHURI
SE	SOFTWARE ENGINEERING	J PRASANNA KUMAR
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	SRI RANGARAJU
DBMS LAB	DATABASE MANAGEMENT SYSTEM LAB	Dr. B SANDHYA, V SATHISH
OS LAB	OPERATING SYSTEM LAB	K MURALI KRISHNA, Dr. AKHIL KHARE
MINI PROJECT	MINI PROJECT	Dr. H JAYASREE, K MADHURI, M V R JYOTISREE

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-I: Room No - CS 201

w. e. f. - 31 - 07 - 2017

	1	2	3	4		5	6	7
	09:45 am To 10.35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday		DS (PS)	E1 (VN)		L U N C H	P SEMINAR (MH, MA, AA, KMK)		
Tuesday	ES (DS)	DS LAB BATCH-A (PS,SP) EBS LAB BATCH-B (DS,MM)				E1 (VN)	IS (AA)	AI (SAD)
Wednesday	IS (AA)	AI (SAD)	DS (PS)					
Thursday	DS (PS)	E1 (VN)	ES (DS)	AI (SAD)		DS LAB BATCH-B (PS,SP) EBS LAB BATCH-A (DS,MM)		
Friday		IS (AA)		AI (SAD)		ES (DS)		
Saturday								

LAB LOCATIONS: DS Lab: LAB-7 First Floor

EBS LAB : LAB 6 First Floor

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEM	P SUBHASHINI
ES	PRINCIPLES & APPLICATIONS OF EMBEDDED SYSTEMS	D SIRISHA
IS	INFORMATION SECURITY	ABDUL AZEEM
AI	ARTIFICIAL INTELLIGENCE	SESHAM ANAND
E1	MOBILE COMPUTING	N VIKRAM
DS LAB	DISTRIBUTED SYSTEM LAB	P SUBHASHINI, P SHALINI
EBS LAB	EMBEDDED SYSTEMS LAB	D SIRISHA, M MADHURI
P SEMINAR	PROJECT SEMINAR	M HANMANDLU, M ANUPAMA, ABDUL AZEEM, K M KRISHNA

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-II: Room No - CS 205

w. e. f. - 31 - 07 - 2017

	1	2	3	4		5	6	7	
	09:45 am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm		01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	ES (DS)	DS LAB BATCH-A (NS,AA) EBS LAB BATCH-B (TL,MLN)			L U N C H	AI (BS)	E1 (GM)	IS(PPP)	
Tuesday	AI (BS)	E1 (GM)	DS (NS)			P SEMINAR (JPK,BS,SB,AVKP)			
Wednesday	E1 (GM)	DS (NS)	ES (DS)	IS(PPP)		DS LAB BATCH-B (NS,AA) EBS LAB BATCH-A (TL,MLN)			
Thursday	DS (NS)	AI (BS)		IS(PPP)					
Friday	ES (DS)		E1 (GM)	IS(PPP)					
Saturday									

LAB LOCATIONS: DS Lab: LAB-7 First Floor

EBS LAB : LAB 6 First Floor

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEM	N SABITHA
ES	PRINCIPLES & APPLICATIONS OF EMBEDDED SYSTEMS	D SIRISHA
IS	INFORMATION SECURITY	P PHANI PRASAD
AI	ARTIFICIAL INTELLIGENCE	B SARITHA
E1	MOBILE COMPUTING	G MADHU
DS LAB	DISTRIBUTED SYSTEM LAB	N SABITHA, ABDUL AZEEM
EBS LAB	EMBEDDED SYSTEMS LAB	MLN REDDY, T LAKSHMI
P SEMINAR	PROJECT SEMINAR	J PRASANNA KUMAR, B SARITHA, Dr. B SANDHYA, Dr. A V K PRASAD

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester - 1

(Acad. Year 2017-18 from 11 - 07 - 2017 to 10-11-17)

Section-III: Room No - Seminar Hall

w. e. f. - 31 - 07 - 2017

	1	2	3	4		5	6	7
	09:45 am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	AI (SV)		DS (SP)	IS (PPP)	L U N C H	DS LAB BATCH-A (SP,NS) EBS LAB BATCH-B (TL,MLN)		
Tuesday	IS (PPP)	E1(VN)		ES(TL)		AI(SV)		
Wednesday	ES (TL)	E1(VN)	DS (SP)					
Thursday	DS (SP)	DS LAB BATCH-B (SP,NS) EBS LAB BATCH-A (TL,MLN)				ES (TL)	AI(SV)	
Friday	IS (PPP)		E1(VN)	ES(TL)		P SEMINAR (MH,GVK,SAD,HJ)		
Saturday								

LAB LOCATIONS: DS Lab: LAB-7 First Floor

EBS LAB : LAB 6 First Floor

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEM	P SHALINI
ES	PRINCIPLES & APPLICATIONS OF EMBEDDED SYSTEMS	T LAKSHMI
IS	INFORMATION SECURITY	P PHANI PRASAD
AI	ARTIFICIAL INTELLIGENCE	V SATHISH
E1	MOBILE COMPUTING	N VIKRAM
DS LAB	DISTRIBUTED SYSTEM LAB	P SHALINI, N SABITHA
EBS LAB	EMBEDDED SYSTEMS LAB	T LAKSHMI, MLN REDDY
P SEMINAR	PROJECT SEMINAR	Dr. M HANMANDLU, G VIJAY KUMAR, SESHAM ANAND, Dr. H JAYASREE

TIME TABLE COORDINATOR	CHIEF TIME TABLE	HOD CSED	PRINCIPAL
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