

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 31 - 12 - 2014)

Section-I: Room No – CS 13

w.e.f . 21 - 07 - 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DSA (MLNR)	LST (GVK)	CA (SAD)	DIS (TL)	L U N C H	DSA Lab, Lab-2 Batch-A (MLNR,SV) BE Lab, ECE dept. Batch-B,(VSK, MVK)		
Tuesday	BE (VSK)		LST (GVK)	CA (SAD)		DSA (MLNR)	TUTORIAL / LIBRARY	
Wednesday	LST (GVK)	DSA Lab, Lab-2 Batch-B (MLNR,SV) BE Lab, ECE dept. Batch-C, (VSK, MVK)				DIS (TL)	M-III (GLK)	
Thursday	M-III (GLK)	DSA Lab, Lab-2 Batch-C (MLNR,SV) BE Lab, ECE dept. Batch-A, (VSK, MVK)				CA (SAD)	BE (VSK)	DSA (MLNR)
Friday	CA (SAD)	DSA (MLNR)	LST (GVK)	M-III (GLK)		BE (VSK)	DIS (TL)	
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS – III	GLK – G LAKSHMI KAMESHWARI
DSA	DATA STRUCTURES	MLNR – M LAKSHMI NARASIMHA REDDY
DIS	DISCRETE STRUCTURES	TL – T LAKSHMI
LST	LOGIC AND SWITCHING THEORY	GVK – G VIJAY KUMAR
CA	COMPUTER ARCHITECTURE	SAD – SESHAM ANAND
BE	BASIC ELECTRONICS	VSK – V SURESH KUMAR
DSA LAB	DATA STRUCTURES LAB	MLNR – M LAKSHMI NARASIMHA REDDY SV – V SATISH
BE LAB	BASIC ELECTRONICS LAB	VSK – V SURESH KUMAR MVK – M VIJAY KRISHNA

Batch A - 2451-12-733-001 TO 024 (24)
 Batch B - 2451-12-733-025 TO 048 (24)
 Batch C - 2451-12-733-049 TO 060, Lateral Entry (301 to 312) (24)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
-----------------------------------	---	---------------------	------------------

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 31 - 12 - 2014)

Section-II: Room No – CS 12

w.e.f . 21 - 07 - 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	CA (SAD)	DSA Lab, Lab-2 Batch-A (SV, AS) BE Lab, ECE dept. Batch-B, (GM, SM)			L U N I T C H	M-III (DB)	BE (GC)	
Tuesday	DSA (SV)	CA (SAD)	LST (DS)	DIS (BJ)		BE (GC)	TUTORIAL / LIBRARY	
Wednesday	M-III (DB)		LST (DS)	DIS (BJ)		BE (GC)	DSA (SV)	
Thursday	DSA (SV)	M-III (DB)	CA (SAD)	LST (DS)		DSA Lab, Lab-2 Batch-B (SV, AS) BE Lab, ECE dept. Batch-C, (GM, SM)		
Friday	LST (DS)	DSA Lab, Lab-2 Batch-C (SV, AS) BE Lab, ECE dept. Batch-A, (GM, SM)				CA (SAD)	DIS (BJ)	
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS – III	DB – D BHAGYA
DSA	DATA STRUCTURES	SV – V SATISH
DIS	DISCRETE STRUCTURES	BJ – B JANAI AH
LST	LOGIC AND SWITCHING THEORY	DS – D SIRISHA
CA	COMPUTER ARCHITECTURE	SAD – SESHAM ANAND
BE	BASIC ELECTRONICS	GC – G CHANDER
DSA LAB	DATA STRUCTURES LAB	SV – V SATISH AS – A SARITHA
BE LAB	BASIC ELECTRONICS LAB	GC – G MAHENDER SM – SMITHASREE

Batch A - 2451-12-733-061 TO 084 (24)
 Batch B - 2451-12-733-085 TO 108 (24)
 Batch C - 2451-12-733-109 TO 120, Lateral Entry (313 to 324) (24)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
-----------------------------------	---	---------------------	------------------

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE II / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 31 - 12 - 2014)

Section-III: Room No – CS 11

w.e.f . 21 - 07 – 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	BE (MSC)		LST (DK)	CA (MM)	L U N I T C H	M-III (MJ)	DIS (BJ)	
Tuesday	CA (MM)	DSA Lab, Lab-2 Batch-A (SA, PPP) BE Lab, ECE dept. Batch-B, (MSC, DPK)				LST (DK)	TUTORIAL / LIBRARY	
Wednesday	DIS (BJ)	CA (MM)	DSA (SA)			DSA Lab, Lab-2 Batch-B (SA, PPP) BE Lab, ECE dept. Batch-C, (MSC, DPK)		
Thursday	M-III (MJ)		DIS (BJ)	LST (DK)		CA (MM)	BE (MSC)	DSA (SA)
Friday	LST (DK)	BE (MSC)	DSA (SA)	M-III (MJ)		DSA Lab, Lab-2 Batch-C (SA, PPP) BE Lab, ECE dept. Batch-A, (MSC, DPK)		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
M-III	MATHEMATICS - III	MJ – M JYOTHSNA
DSA	DATA STRUCTURES	SA – S AMULYA
DIS	DISCRETE STRUCTURES	BJ – B JANAI AH
LST	LOGIC AND SWITCHING THEORY	DK – D KAVITHA
CA	COMPUTER ARCHITECTURE	MM -
BE	BASIC ELECTRONICS	MSC – M SHARATH CHANDRA
DSA LAB	DATA STRUCTURES LAB	SA – S AMULYA PPP – P PHANI PRASAD
BE LAB	BASIC ELECTRONICS LAB	MSC – M SHARATH CHANDRA DPK – D PRAVEEN KUMAR

Batch A - 2451-12-733-121 TO 144 (24)
 Batch B - 2451-12-733-145 TO 168 (24)
 Batch C - 2451-12-733-169 TO 180, Lateral Entry (325 to 336) (24)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
-----------------------------------	---	---------------------	------------------

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 31 - 12 - 2014)

Section-I: Room No – CS 203

w.e.f. 21 - 07 – 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	SE (PPP)	DBMS Lab, Lab-3 Batch-A (DH,BS) OS Lab, Lab-4 Batch-B (AA, TS)			L U N C H	ALC (Dr.CSR)	OS (AA)	
Tuesday	DBMS (DH)	OS (AA)	DAA (Dr.SB)	MEA (MSRR)		SE (PPP)	TUTORIAL / LIBRARY	
Wednesday	ALC (Dr.CSR)	DBMS Lab, Lab-3 Batch-B (DH,BS) OS Lab, Lab-4 Batch-A (AA, TS)				DAA (Dr.SB)	DBMS (DH)	
Thursday	OS (AA)	DAA (Dr.SB)	ALC (Dr.CSR)			SE (PPP)	MEA (MSRR)	
Friday	DAA (Dr.SB)	SE (PPP)	DBMS (DH)	MEA (MSRR)		Mini-Project Lab, Lab-4 (BVR, DK)		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	DH – D HARITHA
OS	OPERATING SYSTEMS	AA – ABDUL AZEEM
ALC	AUTOMATA LANGUAGE AND COMPUTATION	Dr.CSR – Dr.P CHANDRA SEKHAR REDDY
SE	SOFTWARE ENGINEERING	PPP – P PHANI PRASAD
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	MSRR – M SRI RANGA RAJU
DAA	DESIGN AND ANALYSIS OF	Dr.SB – Dr.B SANDHYA
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	DH – D HARITHA BS – B SARITHA
OS LAB	OPERATING SYSTEMS LAB	AA – ABDUL AZEEM TS – T SUJANAVAN
	MINI PROJECT	BVR – B VENKATRAMANA DK – D KAVITHA

Batch A - 2451-12-733-001 TO 036 (36)
 Batch B - 2451-12-733-037 TO 060, Lateral Entry (301 to 312) (36)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
-----------------------------------	---	---------------------	------------------

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 31 - 12 - 2014)

Section-II: Room No – CS 204

w.e.f . 21 - 07 – 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	MEA (MSRR)	OS (VS)	DAA (Dr.SB)		L U N C H	DBMS Lab, Lab-3 Batch-A (BVR, DH) OS Lab, Lab-4 Batch-B (VS, TS)		
Tuesday	DBMS (BVR)	MEA (MSRR)	SE (TS)			DAA (Dr.SB)	TUTORIAL / LIBRARY	
Wednesday	SE (TS)	DAA (Dr.SB)	OS (VS)	ALC (MA)		DBMS (BVR)	MEA (MSRR)	
Thursday	ALC (MA)	DBMS Lab, Lab-3 Batch-B (BVR, DH) OS Lab, Lab-4 Batch-A (VS, TS)				Mini-Project Lab, Lab-4 (VS, VN)		
Friday	DBMS (BVR)		ALC (MA)			SE (TS)	OS (VS)	
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	BVR – B VENKATRAMANA
OS	OPERATING SYSTEMS	VS – V SRIDHAR
ALC	AUTOMATA LANGUAGE AND COMPUTATION	MA – M ANUPAMA
SE	SOFTWARE ENGINEERING	TS – T SUJANAVAN
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	MSRR – M SRI RANGA RAJU
DAA	DESIGN AND ANALYSIS OF	Dr.SB – Dr.B SANDHYA
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	BVR – B VENKATA RAMANA DH – D HARITHA
OS LAB	OPERATING SYSTEMS LAB	VS – V SRIDHAR TS – T SUJANAVAN
	MINI PROJECT	VS – V SRIDHAR VN – VIKRAM NARAYANDAS

Batch A - 2451-12-733-061 TO 096 (36)
 Batch B - 2451-12-733-097 TO 114, 116 TO 120, 2451-12-733-180
 Lateral Entry (313 TO 320, 322 TO 324) (35)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
---------------------------	---------------------------------	-------------	-----------

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE III / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 31 - 12 - 2014)

Section-III: Room No – CS 205

w.e.f . 21 - 07 - 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	DBMS (KM)		ALC (MA)		L U N C H	DAA (AS)	OS (MM)	
Tuesday	ALC (MA)	DBMS Lab, Lab-3 Batch-A (KM, Dr.CSR) OS Lab, Lab-4 Batch-B (MM, VS)				SE (TS)	TUTORIAL / LIBRARY	
Wednesday	MEA (MSRR)	DBMS (KM)	ALC (MA)	OS (MM)		Mini-Project Lab, Lab-4 (TS, MA)		
Thursday	DAA (AS)		OS (MM)	MEA (MSRR)		DBMS (KM)	SE (TS)	
Friday	SE (TS)	DBMS Lab, Lab-3 Batch-B (KM, Dr.CSR) OS Lab, Lab-4 Batch-A (MM, VS)				DAA (AS)	MEA (MSRR)	
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DBMS	DATABASE MANAGEMENT SYSTEMS	KM – K MADHURI
OS	OPERATING SYSTEMS	MM – M MADHURI
ALC	AUTOMATA LANGUAGE AND COMPUTATION	MA – M ANUPAMA
SE	SOFTWARE ENGINEERING	TS – T SUJANAVAN
MEA	MANAGERIAL ECONOMICS AND ACCOUNTANCY	MSRR – M SRI RANGA RAJU
DAA	DESIGN AND ANALYSIS OF	AS – A SARITHA
DBMS LAB	DATABASE MANAGEMENT SYSTEMS LAB	KM – K MADHURI Dr.CSR – Dr.P CHANDRA SEKHAR REDDY
OS LAB	OPERATING SYSTEMS LAB	MM – M MADHURI VS – V SRIDHAR
	MINI PROJECT	TS – T SUJANAVAN MA – M ANUPAMA

Batch A - 2451-12-733-121 TO 157 (37)
 Batch B - 2451-12-733-158 TO 179, Lateral Entry (325 TO 335)
 1608-12-733-078, 1608-12-733-091, 2452-12-733-002 (36)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
-----------------------------------	---	---------------------	------------------

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 31 - 12 - 2014)

Section-I: Room No – CS 09

w.e.f . 21 - 07 - 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	EBS (DS)		IS (NP)	L U N C H	T U T O R I A L/ L I B R A R Y	Project Seminars (MA,GVK, SAD, BS, Dr.SB, SA)		
Tuesday	E-I (MVJS)		AI (BS)			DS Lab, Lab-3 Batch-A (PS, KMK) EBS Lab, Lab-6 Batch-B (DS, GVK, PUG)		
Wednesday	AI (BS)	DS (PS)				E-I (MVJS)	IS (NP)	
Thursday	EBS (DS)	DS (PS)	IS (NP)			DS Lab, Lab-3 Batch-B (PS, KMK) EBS Lab, Lab-6 Batch-A (DS, GVK, PUG)		
Friday	E-I (MVJS)	DS (PS)	EBS (DS)			AI (BS)		Tutorial
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	PS – P SUBHASHINI
AI	ARTIFICIAL INTELLIGENCE	BS – B SARITHA
IS	INFORMATION SECURITY	NP – N PRIYANKA
EBS	PRINCIPLES OF EMBEDDED SYSTEMS	DS – D SIRISHA
E-I (SPM)	ELECTIVE – I	MVJS – M V JYOTHISREE
DS LAB	DISTRIBUTED SYSTEMS LAB	PS – P SUBHASHINI KMK – K MURALI KRISHNA
EBS LAB	EMBEDDED SYSTEMS LAB	DS – D SIRISHA GVK – G VIJAY KUMAR PUG – P USHA GAYATREE
	PROJECT SEMINARS	MA – M ANUPAMA GVK – G VIJAY KUMAR SAD – SESHAM ANAND BS – B SARITHA Dr.SB – Dr.B SANDHYA SA – S AMULYA

Batch A - 2451-11-733-001 TO 036 (36)
 Batch B - 2451-11-733-037 TO 052, 054 TO 060,301 TO 312 &
 10-733-055 (36)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
-----------------------------------	---	---------------------	------------------

M.V.S.R. Engineering College, Nadergul (P.O), Hyderabad - 501510
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Time - Table for B.E CSE IV / IV Semester – I
(Acad. Year 2014-15 from 21 - 07 - 2014 to 19 - 10 - 2013)

Section-II: Room No – CE 206

w.e.f. 21 - 07 - 2014

	1	2	3	4	5	6	7	8
	09:45am To 10:35 am	10:35 am To 11:25 am	11:25 am To 12:15 pm	12:15 pm To 01:00 pm	01:00 pm To 01:45 pm	01:45 pm To 02:35 pm	02:35 pm To 03:25 pm	03:25 pm To 04:15 pm
Monday	EBS (PUG)		DS (KMK)	L U N C H	T U T O R I A L / L I B R A R Y	Project Seminars (GVK, SAD, Dr.SB, SA)		
Tuesday	AI (BS)	DS (KMK)				E-I (VN)	IS (NP)	
Wednesday	E-I (VN)	DS (KMK)	EBS (PUG)			DS Lab, Lab-3 Batch-A (KMK, PS) EBS Lab, Lab-6 Batch-B (GVK, PUG, DS)		
Thursday	IS (NP)	E-I (VN)				AI (BS)	Tutorial	
Friday	EBS (PUG)	AI (BS)	IS (NP)			DS Lab, Lab-3 Batch-B (KMK, PS) EBS Lab, Lab-6 Batch-A (GVK, PUG, DS)		
Saturday								

LEGEND:

ABRV.	SUBJECT	FACULTY IN-CHARGE
DS	DISTRIBUTED SYSTEMS	KMK – K MURALI KRISHNA
AI	ARTIFICIAL INTELLIGENCE	BS – B SARITHA
IS	INFORMATION SECURITY	NP – N PRIYANKA
EBS	PRINCIPLES OF EMBEDDED SYSTEMS	PUG – P USHA GAYATREE
E-I	ELECTIVE – I	VN – VIKRAM NARAYANDAS
DS LAB	DISTRIBUTED SYSTEMS LAB	KMK – K MURALI KRISHNA PS – P SUBHASHINI
EBS LAB	EMBEDDED SYSTEMS LAB	GVK – G VIJAY KUMAR PUG – P USHA GAYATREE DS – D SIRISHA
	PROJECT SEMINARS	MA – M ANUPAMA GVK – G VIJAY KUMAR SAD – SESHAM ANAND BS – B SARITHA Dr.SB – Dr.B SANDHYA SA – S AMULYA

Batch A - 2451-11-733-061 TO 095 (35)
 Batch B - 2451-11-733-096 TO 103, 105 & 106, 108 TO 113,
 115 TO 120, 313 TO 324 & 2451-10-733-112 (35)

TIME TABLE COORDINATOR	CHIEF TIME TABLE COORDINATOR	HOD CSED	PRINCIPAL
-----------------------------------	---	---------------------	------------------