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## **FACULTY OF ENGINEERING**

## B.E. 3/4 (CSE) II – Semester (Main) Examination, June 2014

**Subject : Compiler Construction** 

Tiı	me : 3 hours Max. Marks : 75	
	Note: Answer all questions from Part-A. Answer any FIVE questions from Part-B.  PART – A (25 Marks)	
1	What is cross compiler? Why is bootstrapping required to generate cross compilers?	2
2	Write short notes on input buffering.	2
3	Find first and follow set for the following grammar. $S \to aBDh \\ B \to cC \\ C \to bC \epsilon \\ D \to EF \\ E \to g \epsilon \\ F \to f \epsilon$	3
4	Why SLR and LALR are more economical to construct than canonical LR?	2
5	What is SDD? What are the applications of syntax directed translation?	3
6	Describe static scope and dynamic scope.	2
7	Briefly explain type conversion and coercion.	3
8	List out the various types of three address statements represent the following statement in to triple representation $X[\ i\ ]:=y\ ;$	3
9	Construct the DAG for the following statement	3
	x = y * z w = p + y y = y * z p = w - x	
10	State the major properties of dataflow analysis.	2

## **PART – B** (50 Marks)

11 a) Explain the translation phases of a compiler for the given expression. 6 P = i + r \* 60b) Write short notes on LEX tool. 12 Construct CLR parsing table for the below grammar. 10  $S \rightarrow AA$  $A \rightarrow Aalb$ 13 a) What are synthesized attributes and inherited attributes explain with suitable context free grammar. 5 b) Explain data structures for implementing symbol table. 5 5 14 a) Consider the following piece of code for searching an element x in an array A[100] begin location = -1i = 0while (i < 100) do begin if A[i] = x then location = i i = i + 1end end Discuss about garbage collection in detail. 5 15 a) Explain code optimization techniques. 5 b) Explain issues in the code generation. 16 Explain how to compute data flow equations using live variable analysis by considering any flow graph. 10 17 Write short notes on the following: a) Explain problems in top-down parsing. 4 b) Error recovery techniques in various phases c) Discuss using diagram "Displays" 3

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